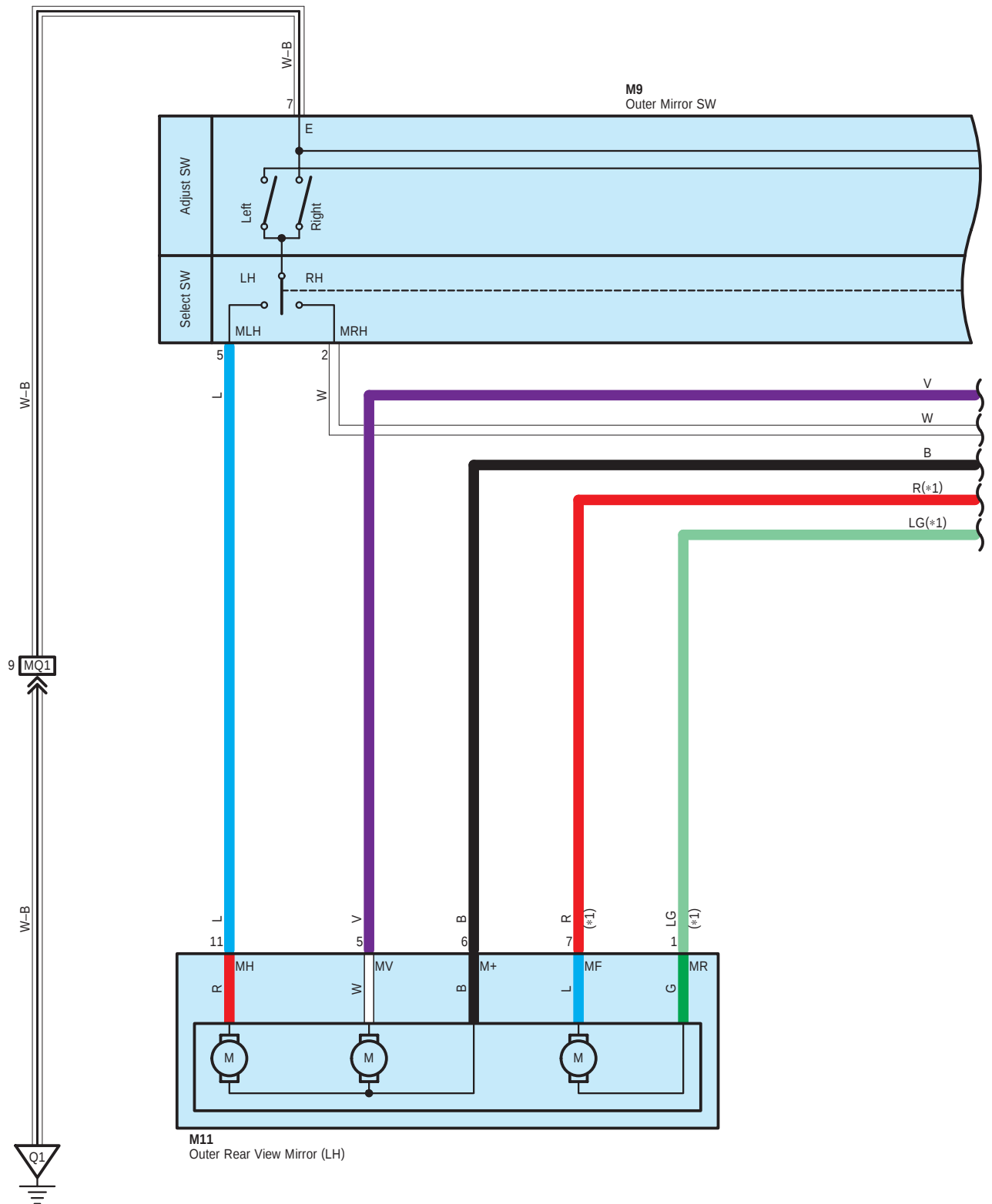
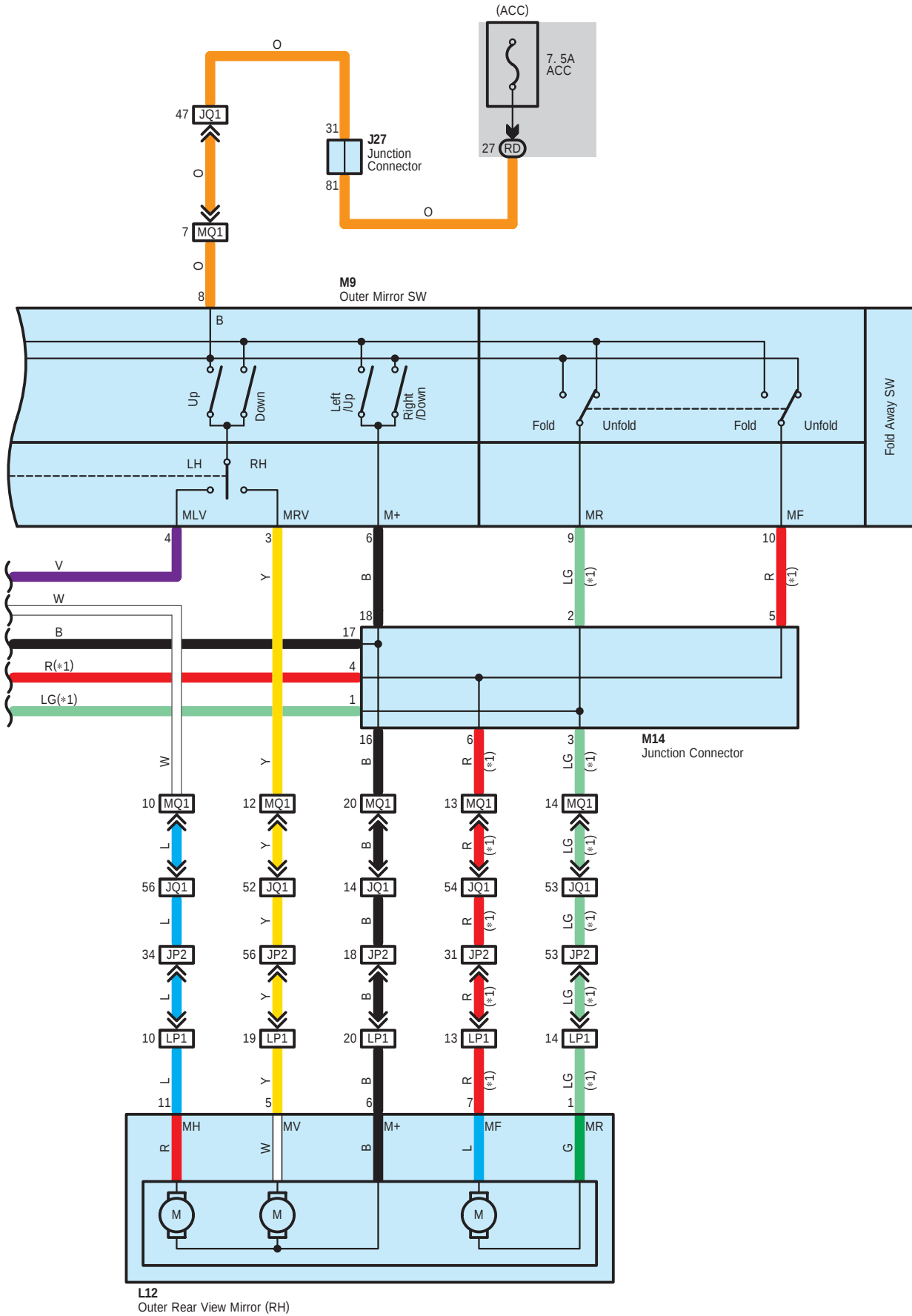




Remote Control Mirror <w/o Seat Position Memory LHD>

* 1 : w/ Retractor



The diagram illustrates a 100-bit bus system. It consists of 10 horizontal rows, each representing a 10-bit data bus. Each row contains 10 numbered boxes (1-10, 11-20, etc.) and a small square box to its right. The rows are connected to a central vertical bus structure at the bottom, which has 100 connection points corresponding to the 100 bits of the system.

A diagram of a 16-key keypad layout. The keypad is rectangular with a central column of four buttons. The top row has two buttons, the second row has three buttons (6, 5, 4), the third row has three buttons (3, 2, 1), the fourth row has three buttons (9, 8, 7), and the bottom row has two buttons. The central column has four buttons, with the top two buttons labeled 11 and 10, and the bottom two buttons labeled 12 and 13. The buttons are arranged in a grid-like fashion with a central column.

(w/o Seat Position Memory)

A diagram of a 10-pin D-sub connector. The pins are numbered 1 through 10 from left to right. Pin 1 is the top pin on the left, and pin 10 is the bottom pin on the right. The connector has a D-shaped shield on the left side.

(w/o Driving Position Memory)

A diagram of a 16-key keypad layout. The keypad is rectangular with a central column of four function keys. The top row has two function keys, the middle row has numbers 6, 5, 4, a function key, 3, 2, 1, and the bottom row has a function key, 11, 10, a function key, 9, 8, 7. The function keys are represented by empty rectangles.

(w/o Seat Position Memory)

1	2	3	4	5	6	7	8	9	10	11
12	13	14	15	16	17	18	19	20	21	22

The diagram illustrates a 32-bit bus architecture. A central horizontal bus is connected to three data paths. The first path on the left has 6 data lines labeled 1 through 6. The second path in the middle has 4 data lines labeled 7 through 10. The third path on the right has 12 data lines labeled 11 through 32. The bus is shown as a series of connected segments, with each data path branching off at a specific point.

The diagram shows a 60-pin connector with two rows of pins. The top row contains pins 1 through 22, and the bottom row contains pins 39 through 60. The pins are numbered as follows:

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22
39	40	41	42	43	44	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38
						45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60

Remote Control Mirror <w/o Seat Position Memory LHD>

