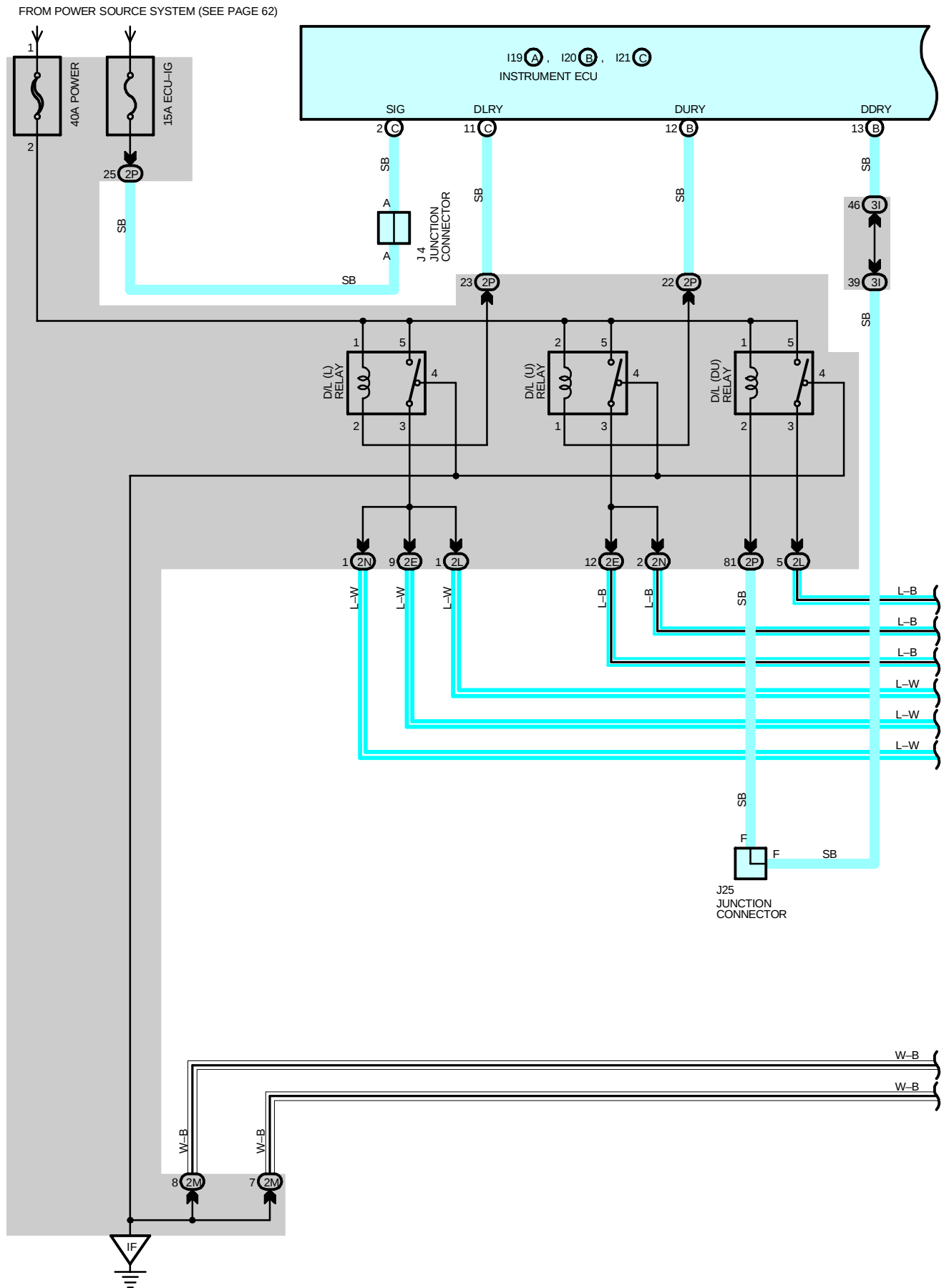
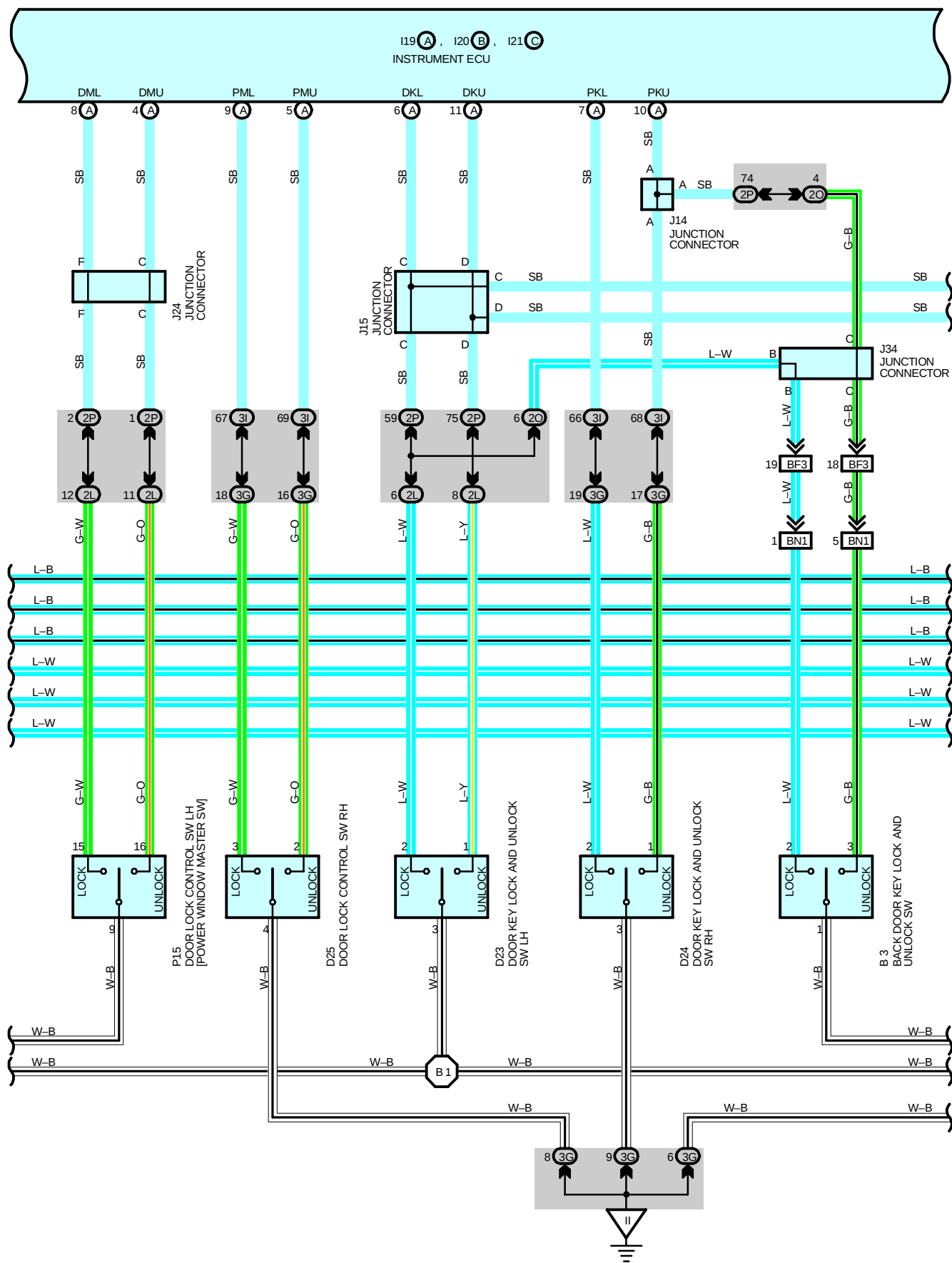
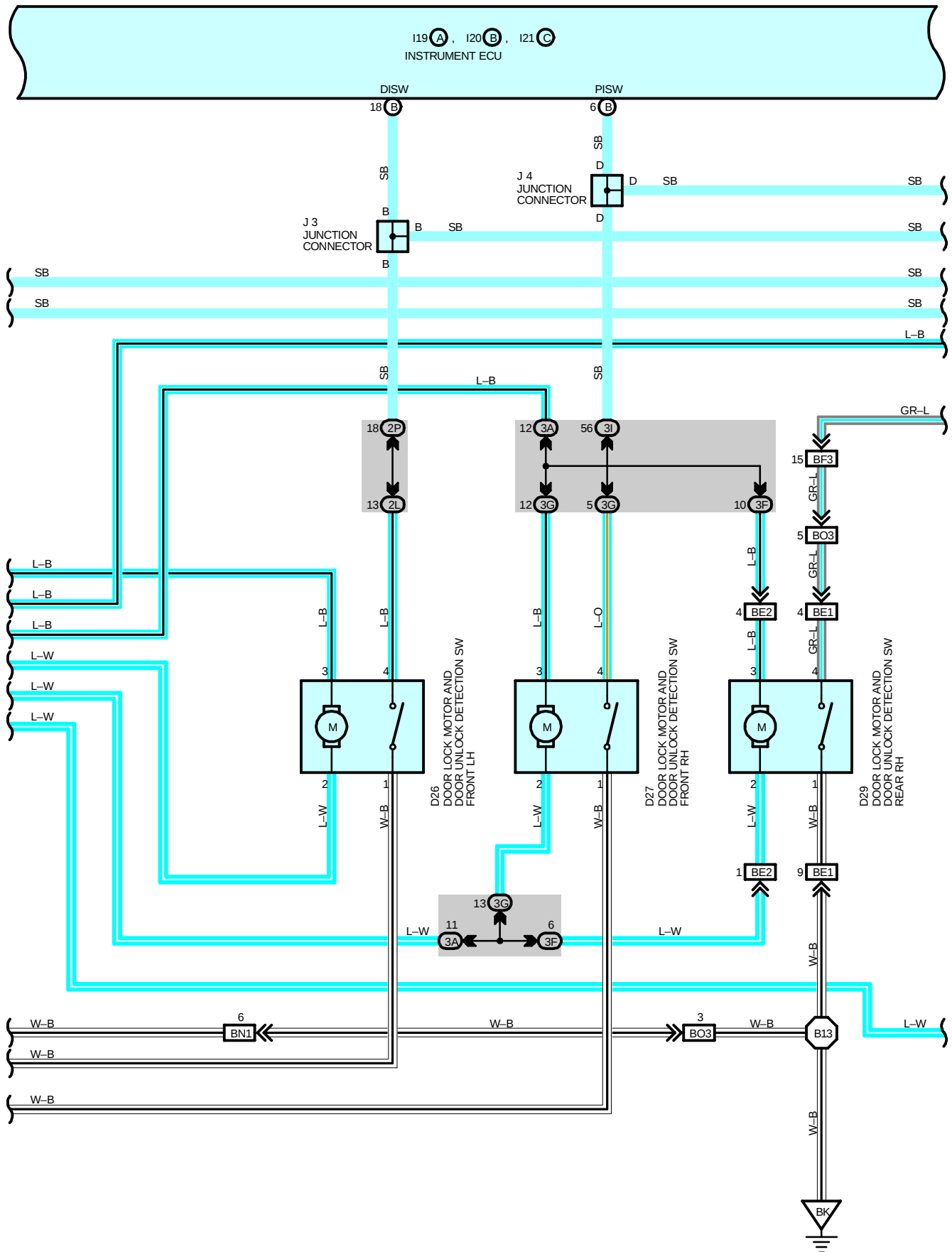


WIRELESS DOOR LOCK CONTROL

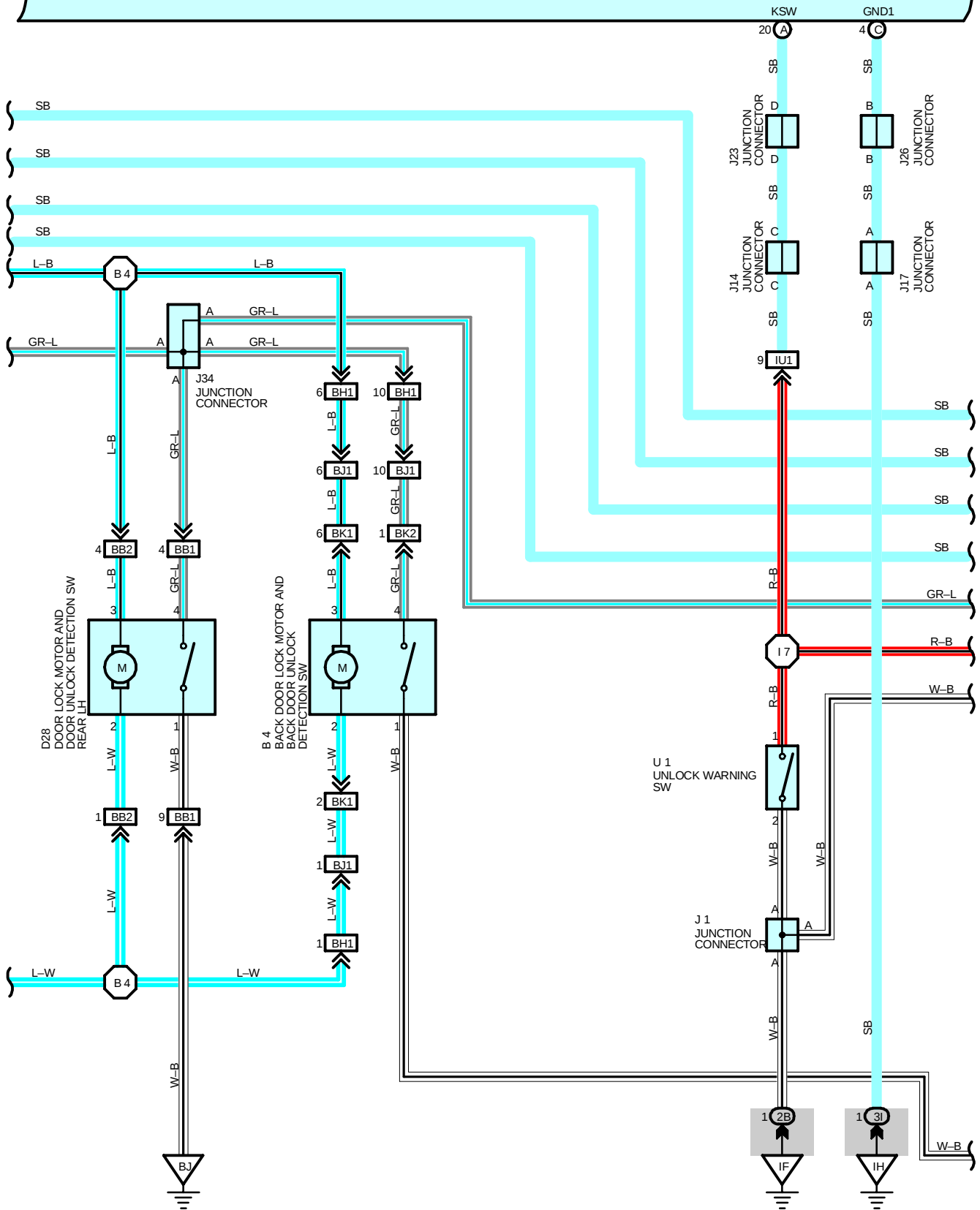


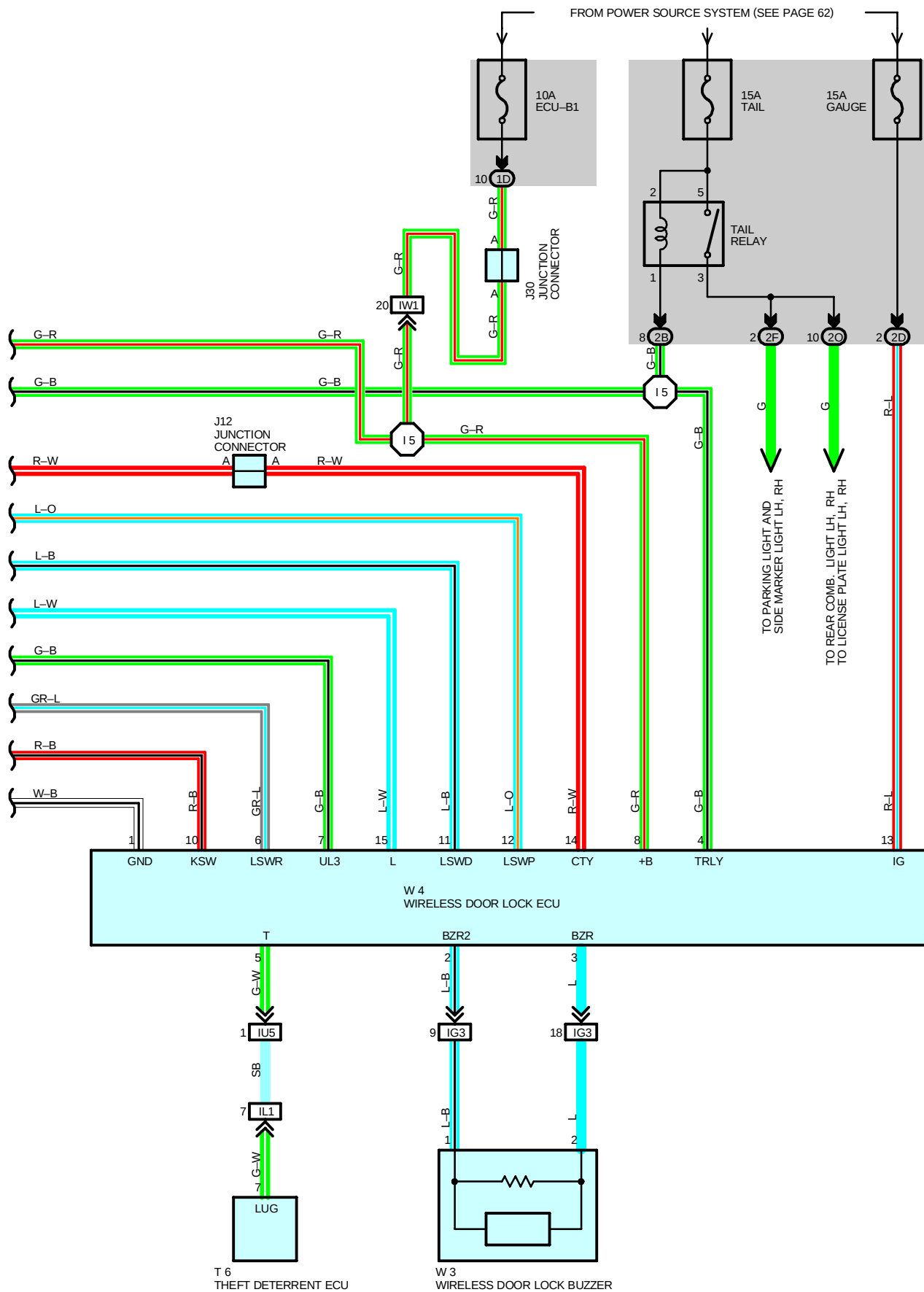


WIRELESS DOOR LOCK CONTROL



I19 (A), I20 (B), I21 (C)
INSTRUMENT ECU





WIRELESS DOOR LOCK CONTROL

SYSTEM OUTLINE

In this system, the wireless door lock ECU receives weak radio wave transmitted from the transmitter built-into the ignition key, and outputs a signal to the instrument ECU. Accordingly, all the doors are can be locked and unlocked by remote control.

1. NORMAL OPERATION

* Lock operation

When the lock SW on the transmitter is pressed, all the doors are locked.

* Unlock operation

When the unlock SW on the transmitter is pressed once, only the driver door is unlocked. When the unlock SW is pressed again within 3 seconds, all the doors are unlocked.

2. AUTO LOCK FUNCTION

When the door is not actually opened within 30 seconds after the door has been unlocked by the unlock SW on the transmitter, all the doors are automatically locked. If any of the following conditions are detected, the wireless door lock does not function.

* Any door is opened.

* The ignition key is inserted into the ignition SW.

* When the unlock detection SW of all the doors are locked.

3. WIRELESS DOOR LOCK STOP FUNCTION

If any of the following conditions are detected, the wireless door lock does not function.

Lock operation

* When any door is open (Door courtesy SW on)

* The ignition key is inserted into the ignition SW (Unlock warning SW on)

* Ignition SW is on

Unlock operation

* Ignition SW is on

4. BUZZER SOUND FUNCTION

During lock operation, when the wireless door lock ECU receives a lock signal from the door unlock detection SW, the wireless door lock buzzer goes on once. During unlock operation, when the wireless door lock ECU receives an unlock signal from the door unlock detection SW, the wireless door lock buzzer goes on twice.

With any door open, when the wireless door lock ECU receives a lock signal from the transmitter, the wireless door lock buzzer goes on for approx. 10 seconds. If the door is closed, or if the unlock signal is received from the transmitter while the buzzer is on, the buzzer stops.

5. VISUAL CONFIRMATION OF LOCK OR UNLOCK

During lock operation, when the wireless door lock ECU receives a lock signal from the door unlock detection SW, the taillight and parking light is flashed once. During unlock operation, when the wireless door lock ECU receives an unlock signal from the door unlock detection SW, the taillight and parking light is flashed twice.

6. PANIC MODE FUNCTION

When the panic SW on the transmitter is pressed, the wireless door lock ECU receives a signal and enters the panic mode. The signal input into the theft deterrent ECU from the wireless door lock ECU turns on the theft deterrent horn, and flashes the taillight and headlight. When the panic SW or the unlock SW of the transmitter is pressed during the panic mode, the panic mode is canceled, and the theft deterrent horn stops, and the taillight and headlight are turned off.

7. REPEAT FUNCTION

If the lock detection signal in response to the output signal is not received after the wireless door lock ECU has output the lock signal, the lock signal is output again.

SERVICE HINTS

W4 WIRELESS DOOR LOCK ECU

1-GROUND : Always continuity

8-GROUND : Always approx. 12 volts

13-GROUND : Approx. 12 volts with ignition SW at ON or ST position

D26, D27, D28, D29 DOOR LOCK MOTOR AND DOOR UNLOCK DETECTION SW FRONT LH, RH, REAR LH, RH

2-GROUND : Approx. 12 volts with door lock motor at lock operation

3-GROUND : Approx. 12 volts with door lock motor at unlock operation

B4 BACK DOOR LOCK MOTOR AND BACK DOOR UNLOCK DETECTION SW

2-GROUND : Approx. 12 volts with door lock motor at lock operation

3-GROUND : Approx. 12 volts with door lock motor at unlock operation

○ : PARTS LOCATION

Code	See Page	Code	See Page	Code	See Page
B2	40	D29	40	J24	39
B3	40	I19	A	J25	39
B4	40	I20	B	J26	39
D19	40	I21	C	J30	39
D20	40	J1	39	J32	40
D21	40	J3	39	J33	40
D22	40	J4	39	J34	40
D23	40	J12	39	P15	41
D24	40	J14	39	T6	39
D25	40	J15	39	U1	39
D26	40	J17	39	W3	37
D27	40	J20	39	W4	39
D28	40	J23	39		

○ : JUNCTION BLOCK AND WIRE HARNESS CONNECTOR

Code	See Page	Junction Block and Wire Harness (Connector Location)
1D	21	Engine Room No.2 Wire and Engine Room J/B (Engine Compartment Left)
2B	24	Dash Wire and Cowl Side J/B LH (Left Kick Panel)
2D		
2E	24	Engine Room No.2 Wire and Cowl Side J/B LH (Left Kick Panel)
2F		
2L	24	Front Door LH Wire and Cowl Side J/B LH (Left Kick Panel)
2M		
2N	24	Floor Wire and Cowl Side J/B LH (Left Kick Panel)
2O		
2P	26	Instrument Panel Integration Wire and Cowl Side J/B LH (Left Kick Panel)
3A	30	Engine Room No.2 Wire and Cowl Side J/B RH (Right Kick Panel)
3E	30	Floor No.2 Wire and Cowl Side J/B RH (Right Kick Panel)
3F		
3G	30	Front Door RH Wire and Cowl Side J/B RH (Right Kick Panel)
3I	30	Instrument Panel Integration Wire and Cowl Side J/B RH (Right Kick Panel)

□ : CONNECTOR JOINING WIRE HARNESS AND WIRE HARNESS

Code	See Page	Joining Wire Harness and Wire Harness (Connector Location)
ID1	46	Dash Wire and Floor Wire (Left Kick Panel)
IG3	48	Engine Room No.2 Wire and Dash Wire (Behind the Combination Meter)
IL1	48	Instrument Panel Integration Wire and Computer Wire (Instrument Panel Center)
IU1	50	Instrument Panel Integration Wire and Dash Wire (Behind the Glove Box)
IU4		
IU5		
IW1	52	Engine Room No.2 Wire and Dash Wire (Behind the Glove Box)
BB1	54	Rear Door LH Wire and Floor Wire (Left Side of Center Pillar)
BB2		
BE1	54	Rear Door RH Wire and Floor No.2 Wire (Right Side of Center Pillar)
BE2		
BF3	54	Floor No.3 Wire and Floor Wire (Left Rear Side Quarter Panel)
BH1	54	Luggage Room Wire and Floor Wire (Left Rear Side Quarter Panel)
BJ1	56	Pillar No.1 Wire and Luggage Room Wire (Left Rear Side Roof Panel)
BK1	56	Pillar No.1 Wire and Back Door Upper Wire (Left Side of Back Door)
BK2		
BN1	56	Back Door Lower Wire and Floor No.3 Wire (Right Side of Rear Floor Crossmember)
BO3	56	Floor No.2 Wire and Floor No.3 Wire (Right Side of Rear Floor Crossmember)

WIRELESS DOOR LOCK CONTROL



: GROUND POINTS

Code	See Page	Ground Points Location
IF	46	Set Bolt of Cowl Side J/B LH
IH	46	Set Bolt of Cowl Side J/B RH
II		
BJ	54	Under the Driver's Seat
BK	54	Front
BM	54	Left Rear Side Quarter Panel



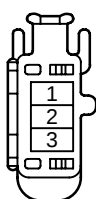
: SPLICE POINTS

Code	See Page	Wire Harness with Splice Points	Code	See Page	Wire Harness with Splice Points
I5	48	Dash Wire	B4	56	Floor Wire
I7			B13	56	Floor No.2 Wire
B1	56	Floor Door LH Wire	B14	56	Back Door Upper Wire

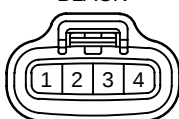
B2



B3



B4
BLACK



D19, D20



D21, D22



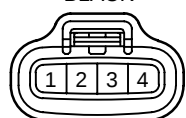
D23, D24
GRAY



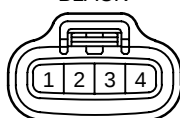
D25



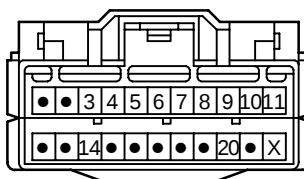
D26, D27
BLACK



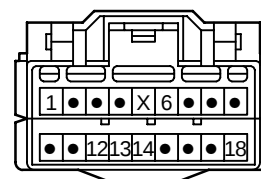
D28, D29
BLACK



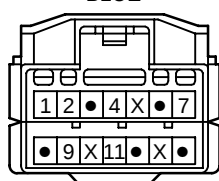
I19 (A)



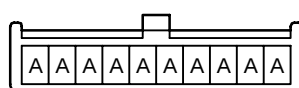
I20 (B)



I21 (C)
BLUE

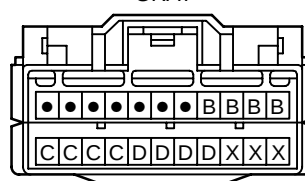


J1



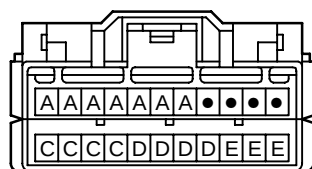
(Hint : See Page 7)

J3
GRAY



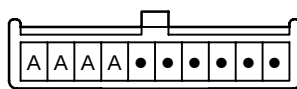
(Hint : See Page 7)

J4



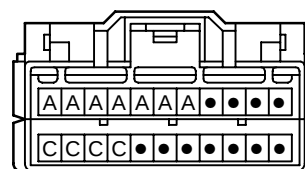
(Hint : See Page 7)

J12
BLUE



(Hint : See Page 7)

J14



(Hint : See Page 7)

A diagram of a 16-pin D-sub connector. The pins are numbered 1 through 16. The labels are as follows: pins 1-4 are labeled 'C', pins 5-8 are labeled 'D', pins 9-12 are labeled 'X', and pins 13-16 are labeled 'B'.

A diagram of a 16-pin D-sub connector. The top row of pins is labeled A through X. The bottom row of pins is labeled 1 through 16. The connector is shown in a perspective view, with the pins protruding from the front face.

A diagram of a 2x8 LED display. The top row shows three dots, followed by the letters 'D' and 'X', and then three more dots. The bottom row shows the same pattern: three dots, 'D', 'X', and three dots.

A diagram of a 2x6 grid-in response area. It consists of two rows of six bubbles each. The top row has bubbles for digits 0, 1, 2, 3, 4, and 5, followed by bubbles for letters B and X. The bottom row has bubbles for digits 6, 7, 8, and 9, followed by bubbles for letters B and X. The bubbles are arranged in a 2x6 grid.

A diagram of a 10-bit shift register. It consists of a horizontal row of 10 square cells, each containing the letter 'A'. Above the cells is a horizontal bar with a central notch, and below the cells is a horizontal bar with a central protrusion, representing the clock and data lines of the register.

A diagram of a 10-bit shift register. It consists of a horizontal row of 10 square cells, each containing the letter 'A'. Above the cells is a single input line with a small rectangular block in the center. Below the cells is a single output line with a small rectangular block in the center.

A diagram of a 10-cell array. The first four cells contain the letter 'A', and the next six cells contain the letter 'B'. The array is represented as a horizontal row of 10 boxes, with the first four boxes labeled 'A' and the remaining six boxes labeled 'B'.

The diagram illustrates the internal structure of a 16-bit ALU. It consists of two 8-bit adders connected in series. The first adder takes an 8-bit input 'a' and an 8-bit input 'b'. The second adder takes the 8-bit output from the first adder and an 8-bit input 'c'. The final 16-bit output is shown as a sequence of bits: 9, 15, 16, and a final bit. The diagram also shows the carry chain and the internal logic of the adders.

A schematic diagram of a mechanical assembly, likely a valve or actuator. It features a central rectangular body with two distinct sections labeled '1' and '2'. Section 1 is on the left and section 2 is on the right. The assembly has various ports and connections, including a large circular port at the top and several smaller ports on the sides and bottom.

A diagram of a two-chambered heart cross-section. The heart is divided into two main chambers, labeled 1 and 2. Chamber 1 is on the left and chamber 2 is on the right. They are separated by a vertical septum. Above the chambers is a single inlet opening. Below the chambers is a single outlet opening. The heart is surrounded by a pericardial cavity.

Diagram of a 16-bit ALU with two 8-bit registers. The left register contains 10101010 and the right register contains 01010101. The ALU performs a subtraction (10101010 - 01010101) resulting in 01010101. The ALU is labeled with "16-bit ALU" and "8-bit" for the registers.