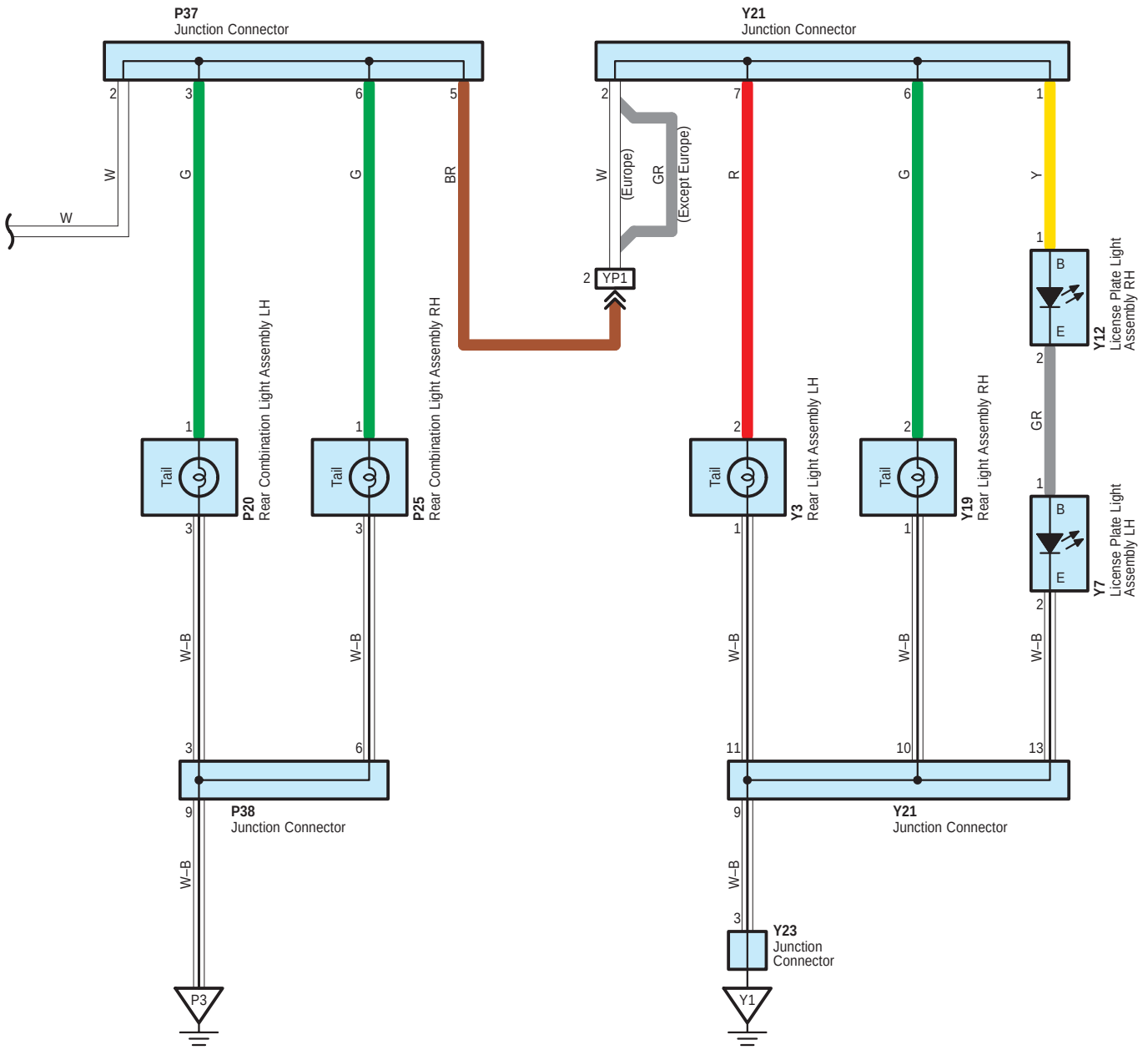


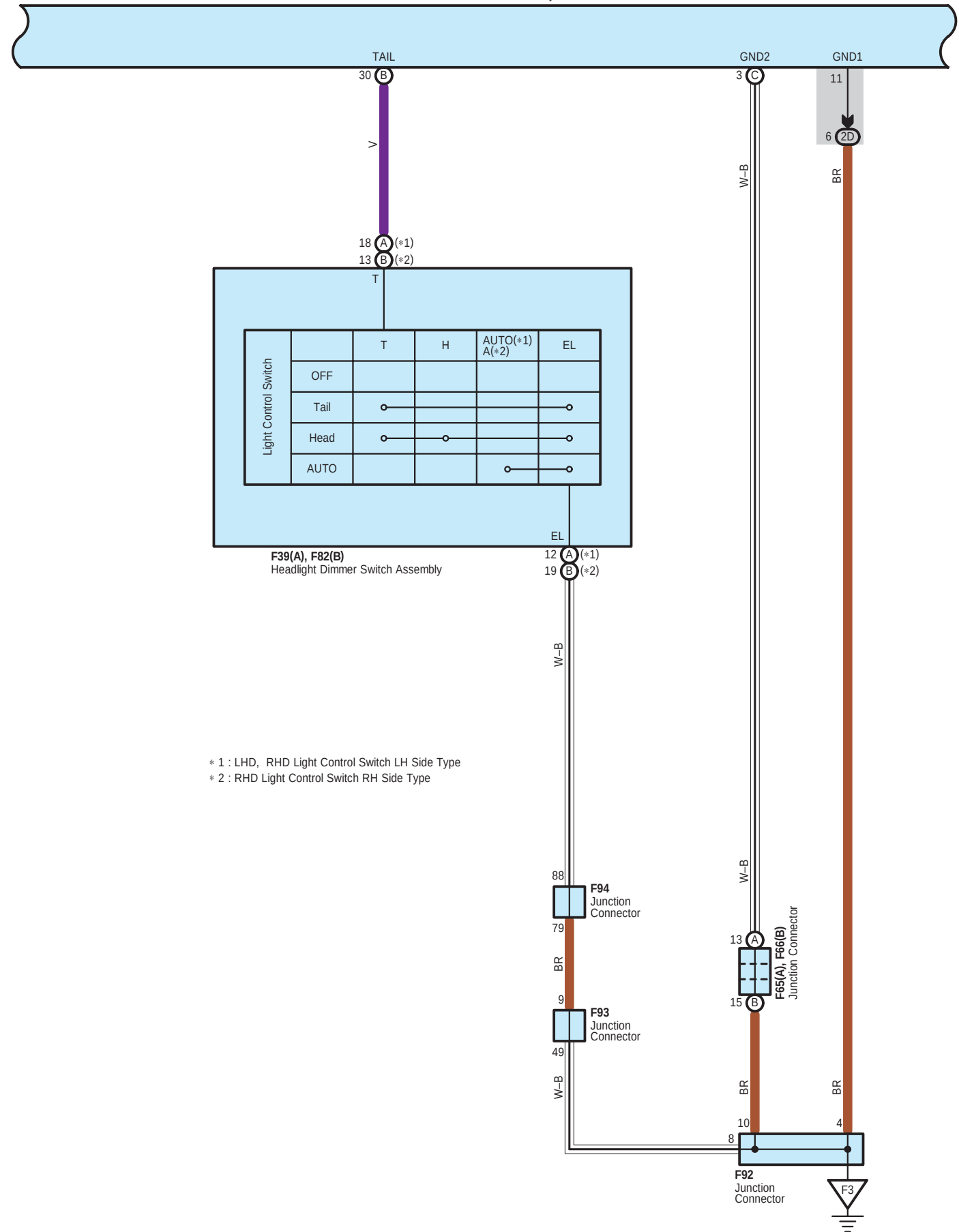


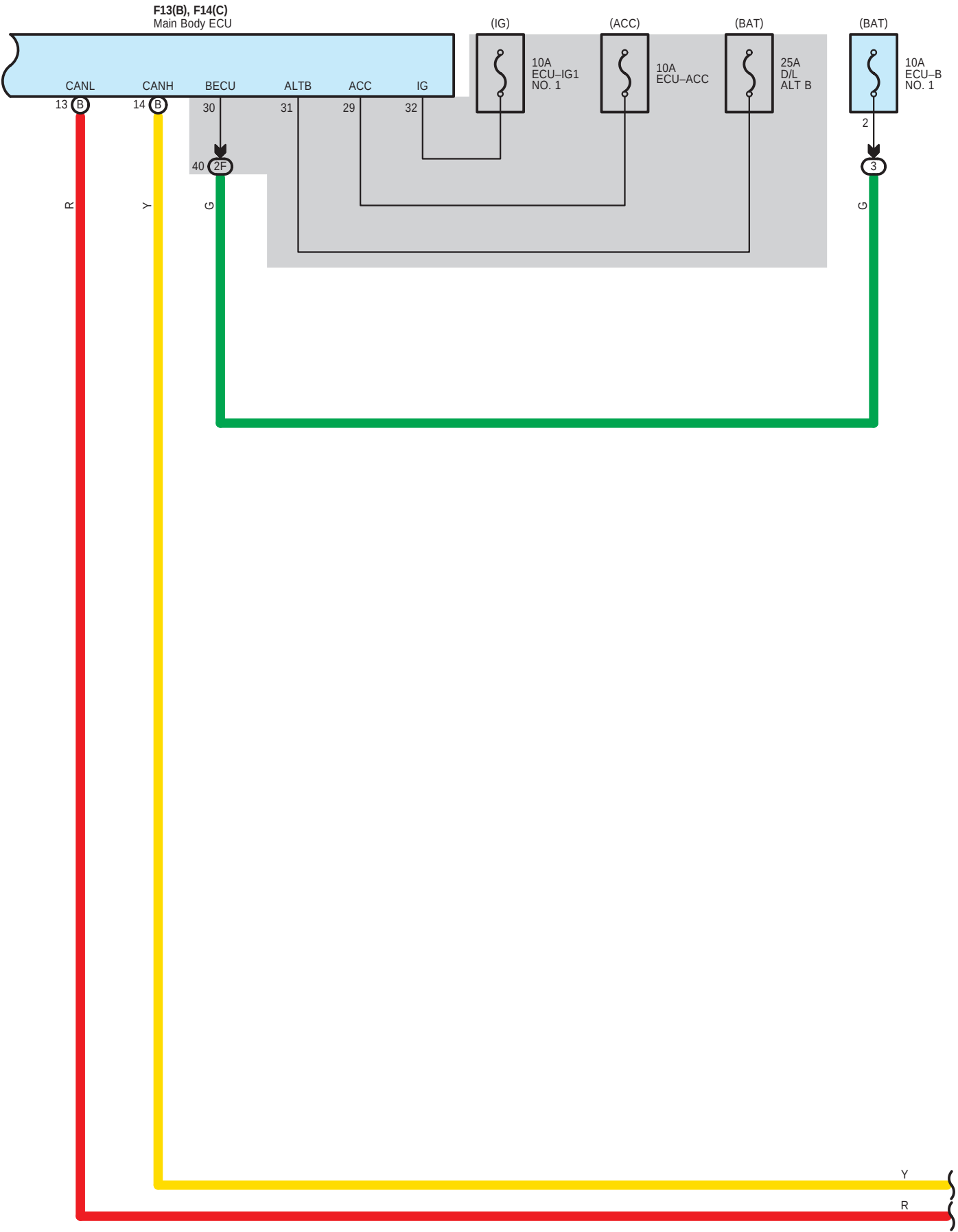
Taillight

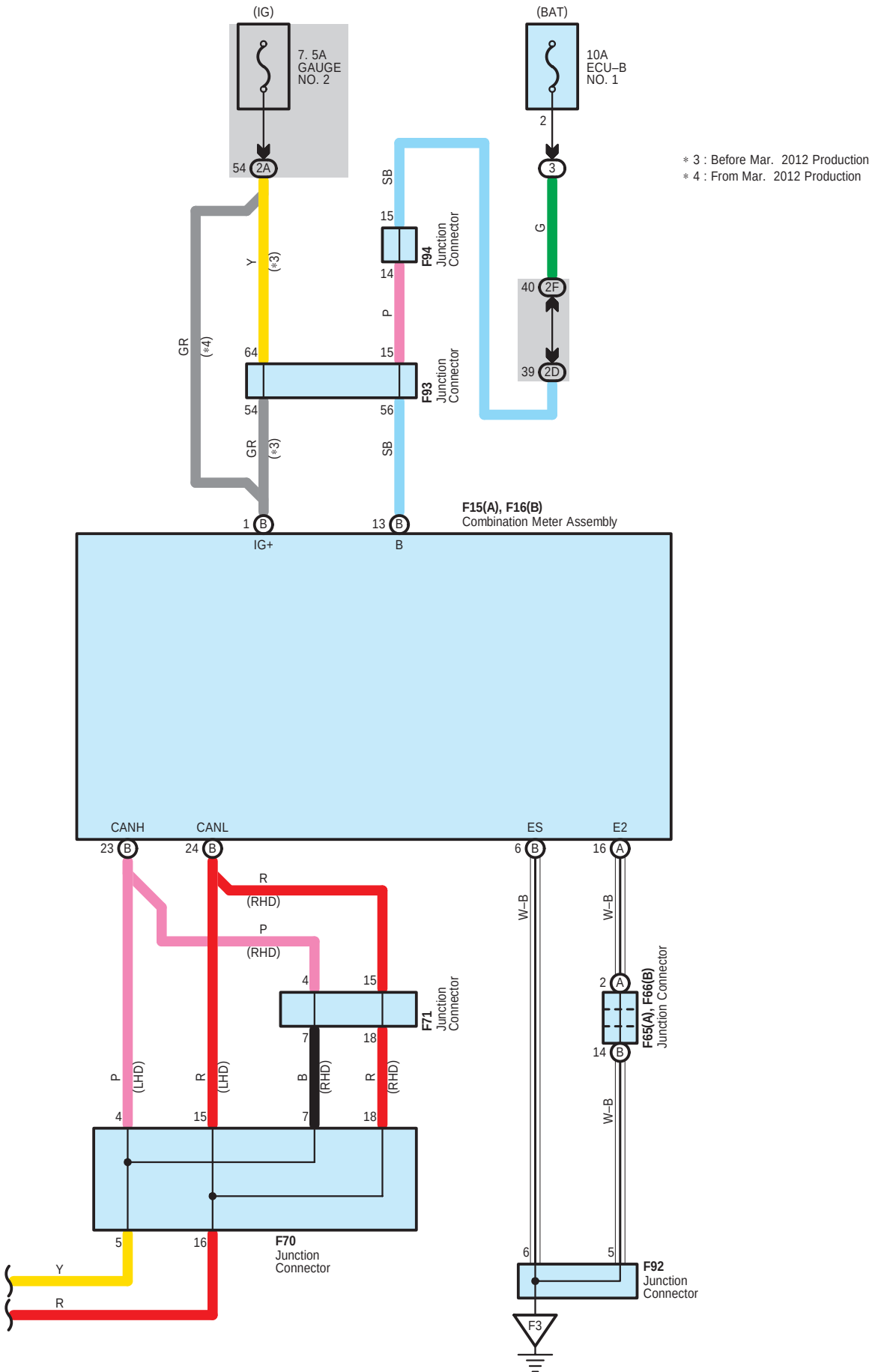
F13(B), F14(C)
Main Body ECU

- * 3 : Before Mar. 2012 Production
- * 4 : From Mar. 2012 Production
- * 5 : Before Aug. 2013 Production
- * 6 : From Aug. 2013 Production









A schematic diagram of a two-chambered battery. It consists of a central rectangular block divided into two equal halves, labeled '1' and '2'. This block is enclosed within a larger, irregular outer frame. At the top of the frame is a horizontal terminal with a small protrusion in the center. At the bottom of the frame is another horizontal terminal, also with a small protrusion in the center. The entire assembly is shown in a cross-sectional or top-down view.

A schematic diagram of a battery cell, showing a central rectangular area divided into two sections labeled '1' and '2'. This central area is enclosed within a larger, irregular frame representing the battery housing. At the top of the frame is a handle-like structure, and at the bottom is a terminal or connector. The diagram illustrates the internal layout of the battery cell.

A diagram of a 22-pin connector. It features a 2x11 grid of pins. The top row is numbered 1 through 11 from left to right. The bottom row is numbered 12 through 22 from left to right. Above the grid, there are several colored lines (purple, blue, green, yellow) representing different signal paths or data buses connecting to the pins.

A top-down diagram of a 28-pin Dual In-Line Package (DIP). The package is rectangular with rounded corners and a central notch. It features two rows of pins, with pins 1 through 14 on the top row and pins 15 through 28 on the bottom row. Pin 1 is located at the top-left corner, and pin 28 is at the bottom-right corner. The diagram includes mounting tabs on the top and bottom edges and a central mounting tab on the top edge.

A diagram of a 16-bit shift register. It consists of a horizontal row of 16 cells, numbered 1 to 16 from left to right. Above the cells is a clock input line with a triangle symbol pointing to it. The cells are currently empty, representing a zero state.

A diagram of a card reader interface. It features a 2x12 grid of slots for cards. Above the grid is a numeric keypad with digits 1 through 24 arranged in two rows. The top row contains digits 1-12, and the bottom row contains digits 13-24. The keypad is integrated into the top of the card reader frame.

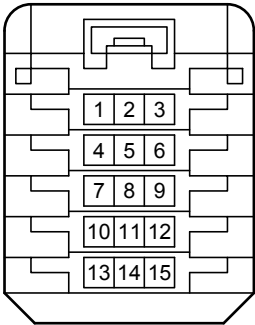
A diagram of a 20-pin D-sub connector. The pins are arranged in two rows of ten. The top row is numbered 1 to 10 from left to right. The bottom row is numbered 11 to 20 from left to right. The connector has a D-shaped shield and a latch mechanism on the right side.

A 4x4 grid representing a 15-puzzle state. The grid contains 15 numbered tiles and one blank space. The tiles are arranged as follows:

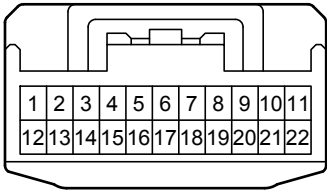
	1	2	3
	4	5	6
	7	8	9
	10	11	12
	13	14	15

The blank space is located at the top center of the grid, between the top edge and the first row of tiles.

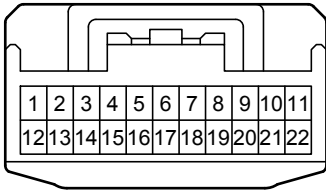
F 66
White



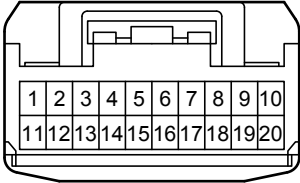
F 70
Blue



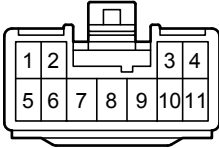
F 71
Blue



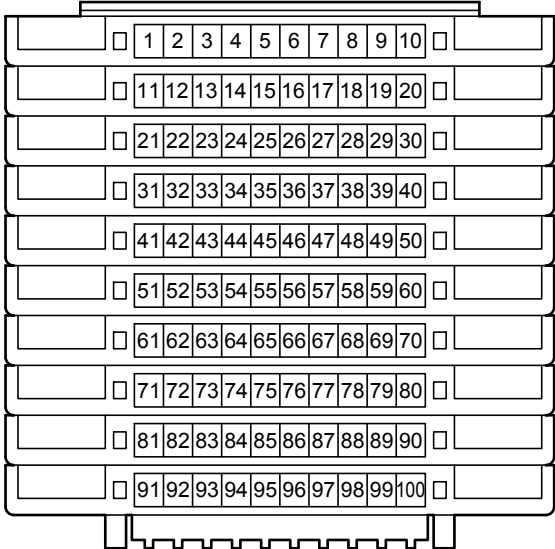
F 82
White



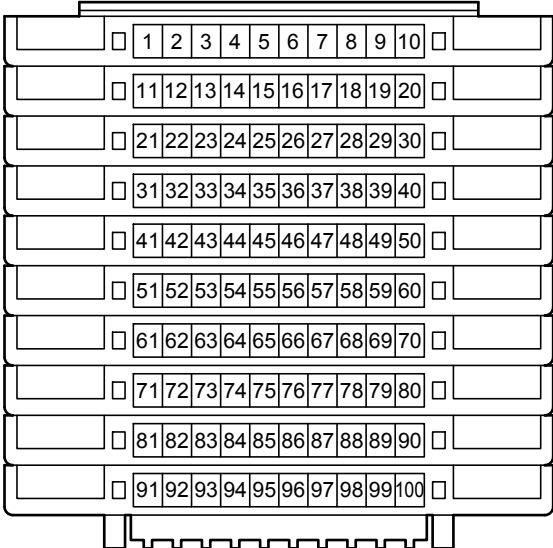
F 92
White



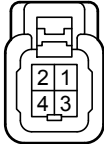
F 93
White



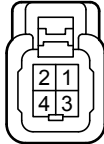
F 94
White



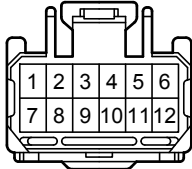
P 20
White



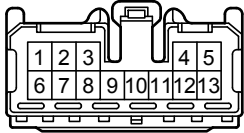
P 25
White



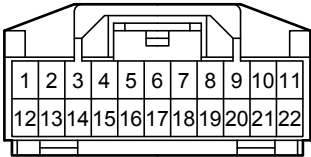
P 37
Blue



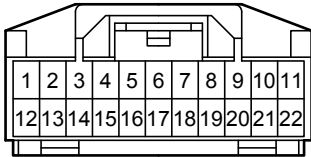
P 38
Black



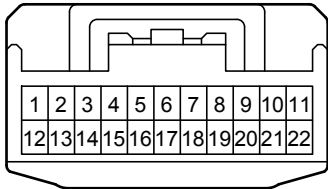
P 39
Blue



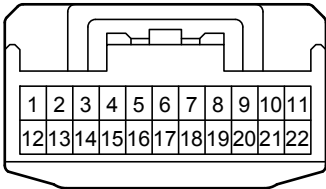
P 40
Blue



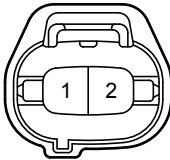
P 49
Blue



P 50
Blue



Y 3
Black



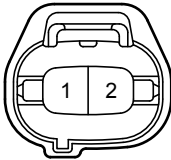
Y 7
Gray



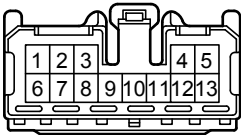
Y 12
Gray



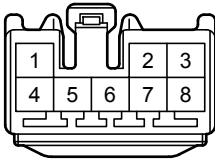
Y 19
Black



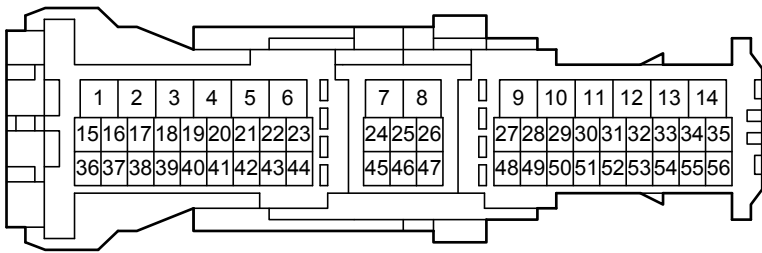
Y 21
Gray



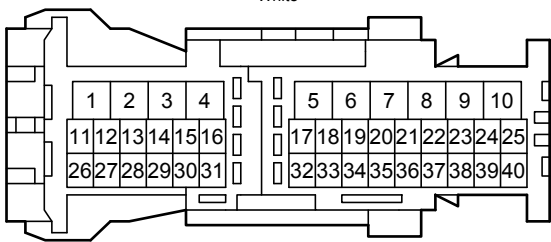
Y 23
White



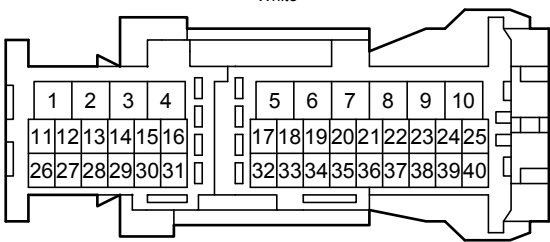
2A
White



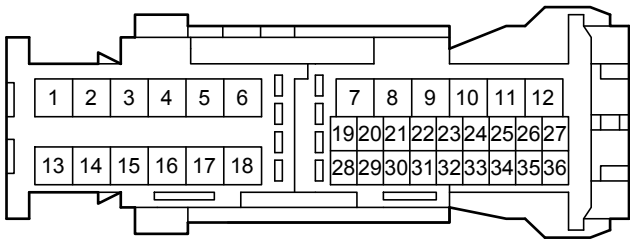
2D
White



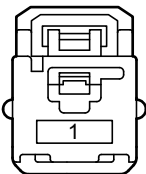
2F
White



2H
White



2K
White



3G
White

