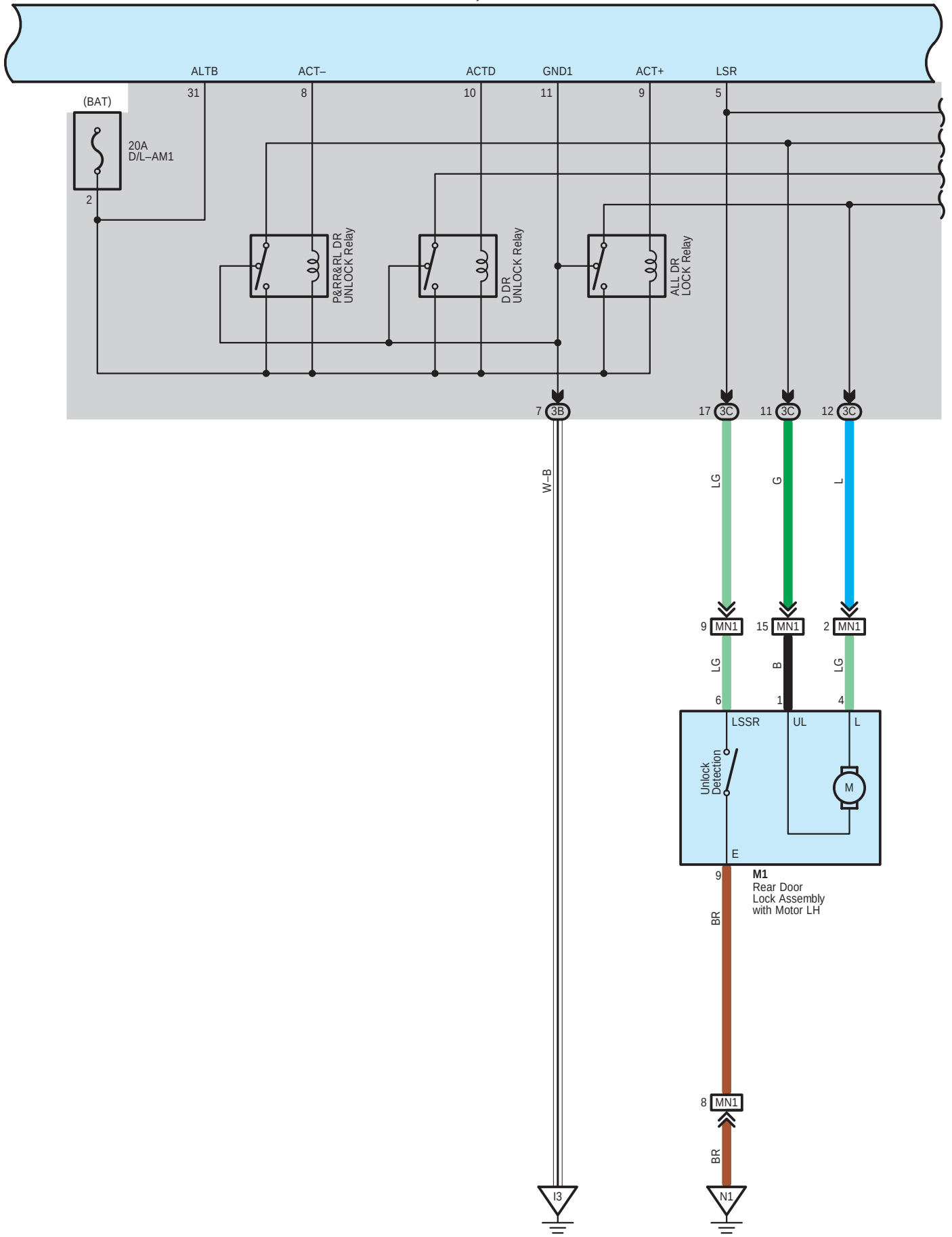
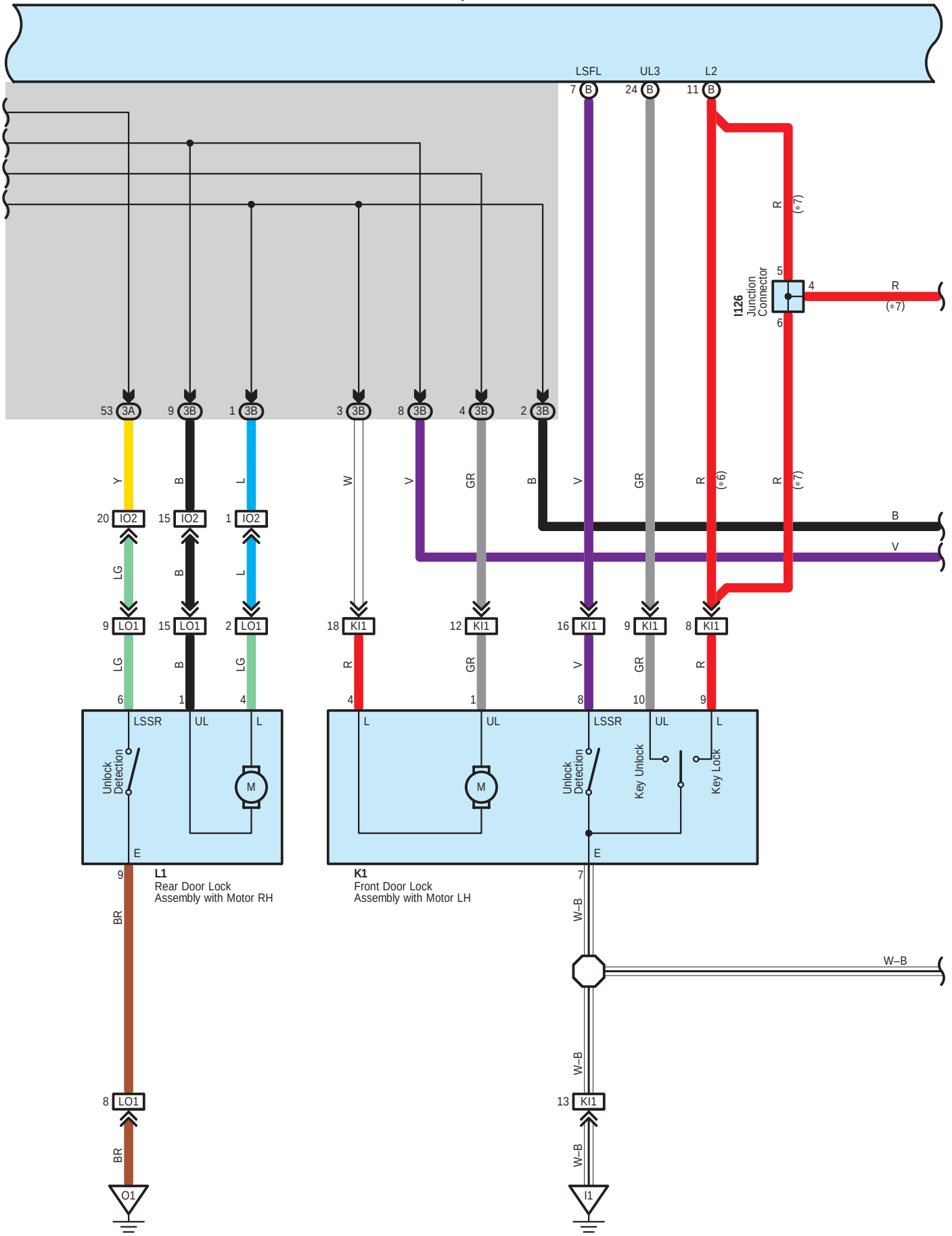

Door Lock Control

I12(A), I13(B)
Main Body ECU

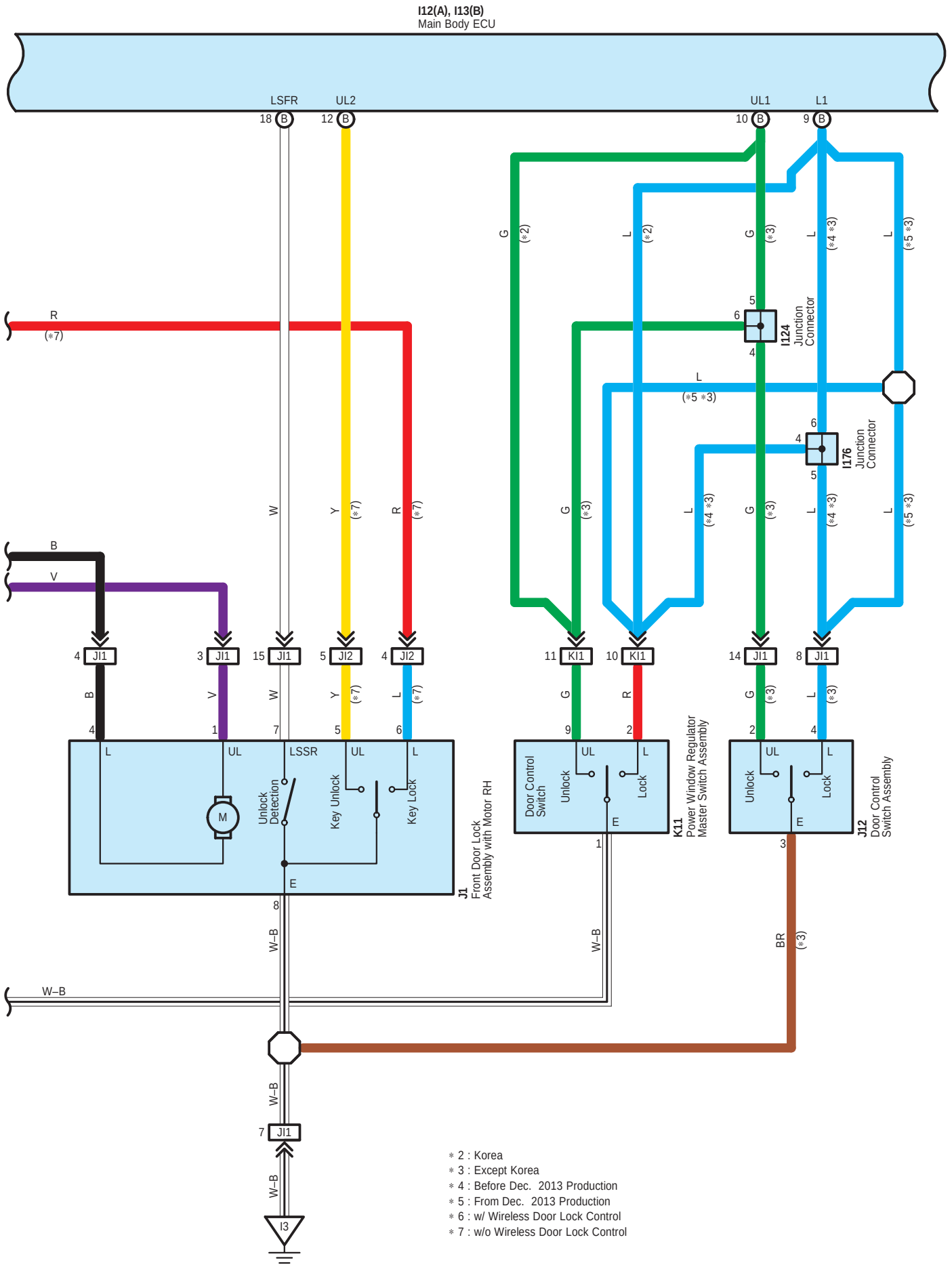


Door Lock Control

I12(A), I13(B)
Main Body ECU

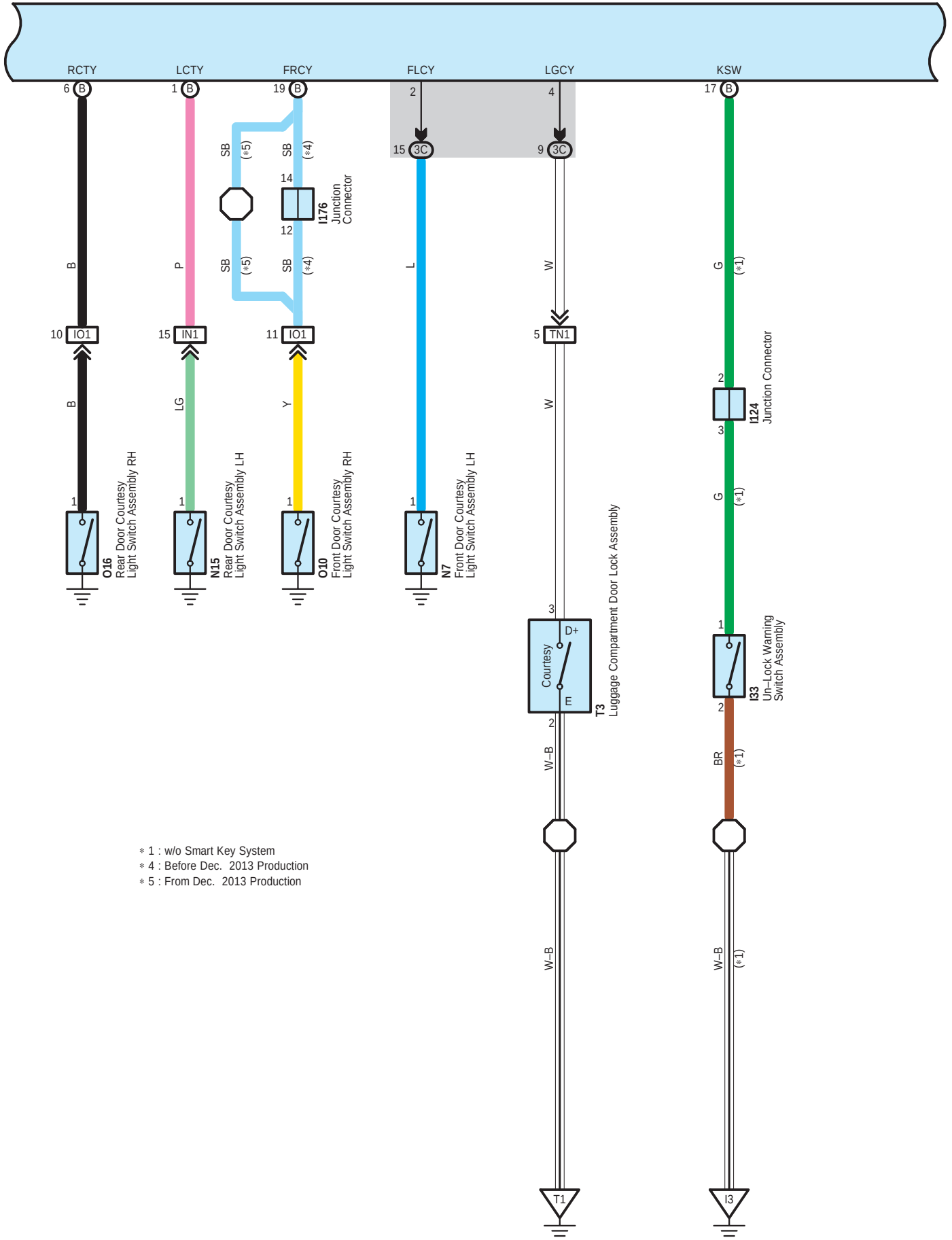


Door Lock Control

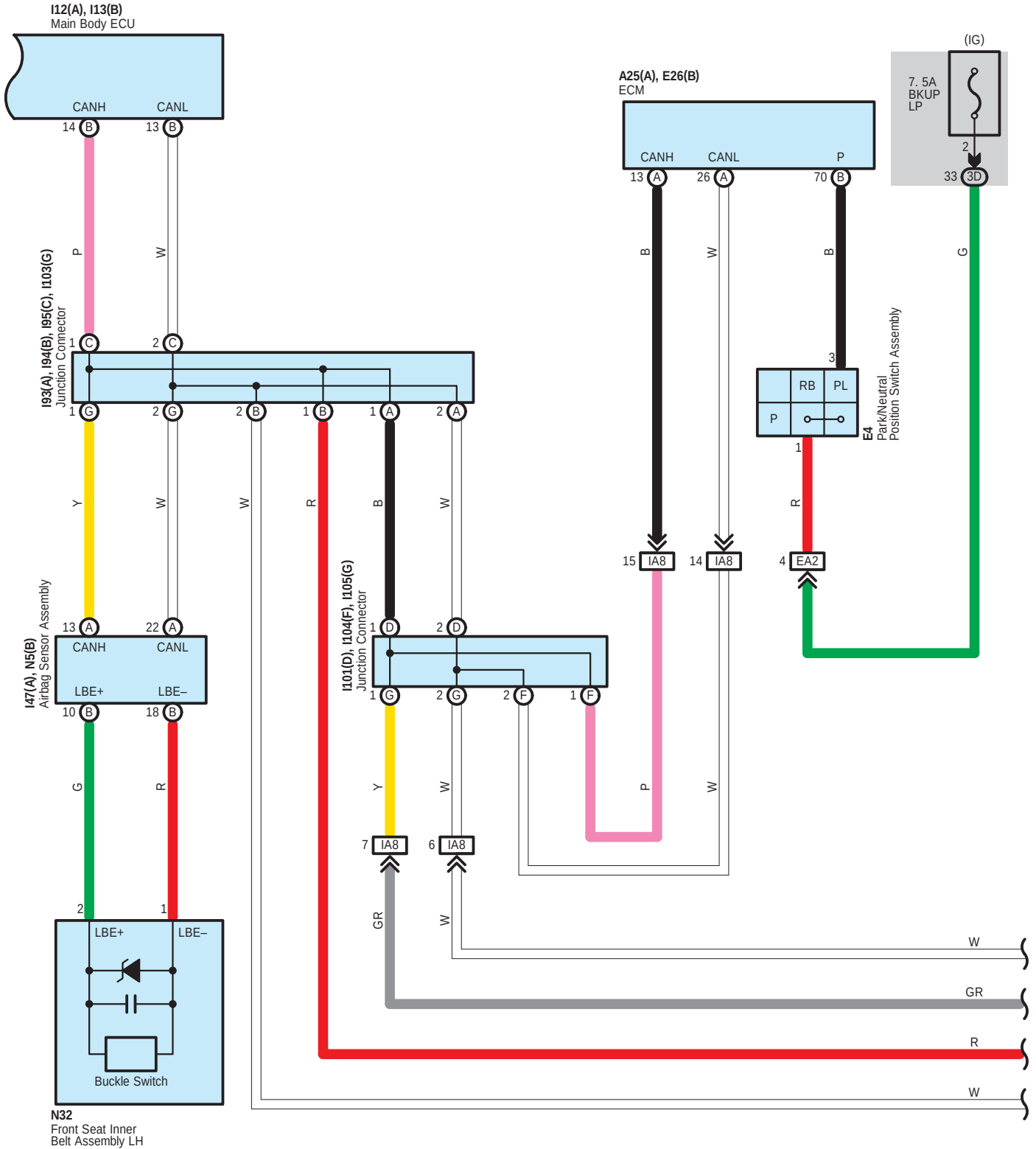


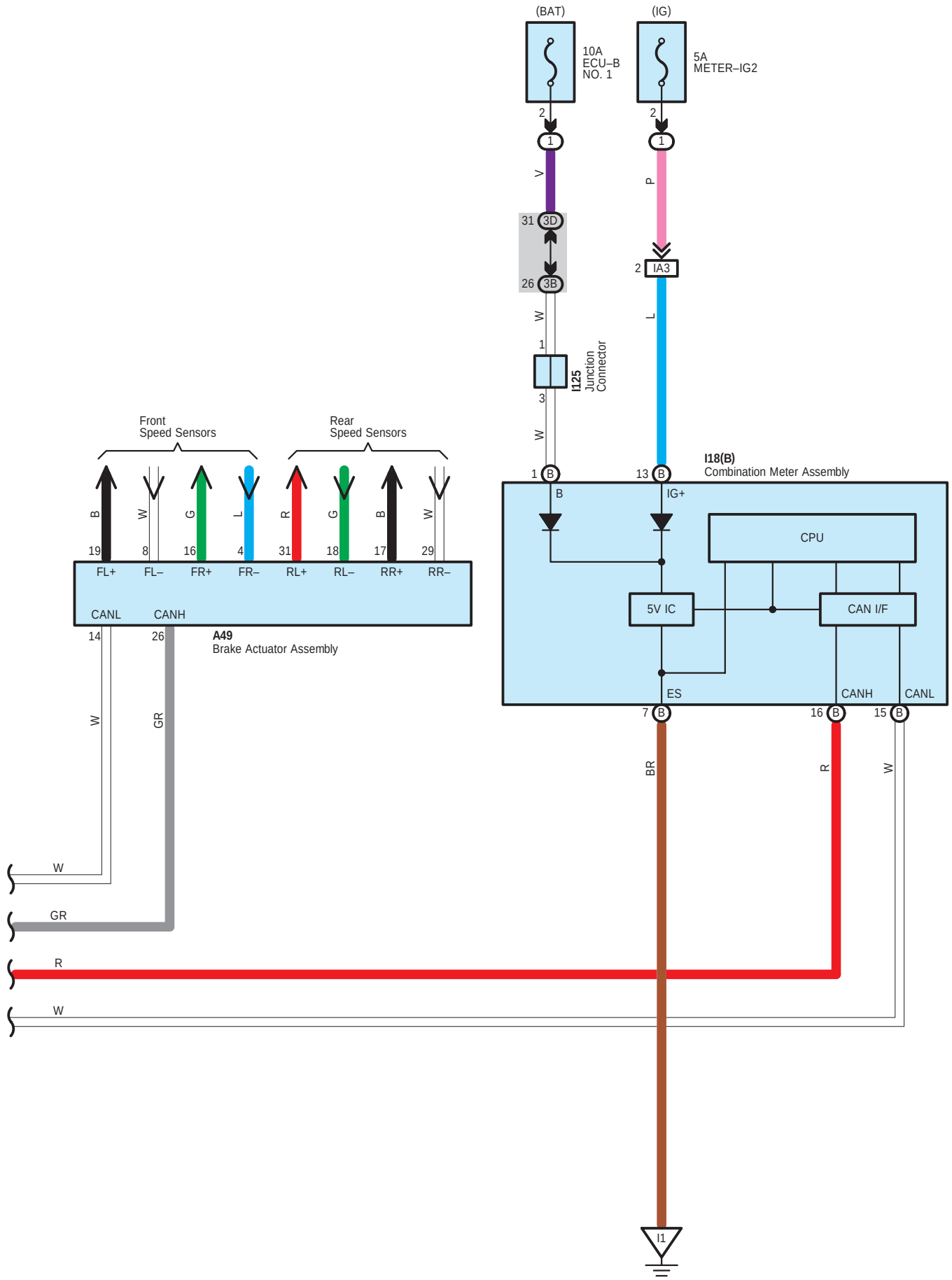
Door Lock Control

I12(A), I13(B)
Main Body ECU



Door Lock Control





The diagram shows a front view of a card reader with a central panel containing two sets of slot grids. The left grid is a 3x8 matrix of slots numbered 1 through 41, with the last slot empty. The right grid is a 3x8 matrix of slots numbered 22 through 60. The entire device is shown within a rectangular frame with mounting tabs on the sides and bottom.

1	2	3	4	5	6	7	8	9	10	11	12	13
14	15	16	17	18	19	20	21	22	23	24	25	26
32	33	34	35	36	37	38	39	27	28	29	30	31
47	48	49	50	51	52	53	54	42	43	44	45	46
55	56							57	58	59	60	

1	2	3	4	5	6	7
31	32	33	34	35	36	37
61	62	63	64	65	66	67
76	77	78	79	80	81	82
109	110	111	112	113	114	

8	9	10	11	12	13	14	15
38	39	40	41	42	43	44	45
68	69	70	71	72	73	74	75
83	84	85	86	87	88	89	90
115	116	117	118	119	120	121	122

16	17	18	19	20	21	22	23		
46	47	48	49	50	51	52	53		
91	92	93	94	95	96	97	98	99	100
123	124	125	126	127	128	129	130	131	132

24	25	26	27	28	29	30	
54	55	56	57	58	59	60	
101	102	103	104	105	106	107	108
133	134	135	136	137	138	139	140

A detailed diagram of a 28-pin Dual In-Line Package (DIP) component. The package is shown from a top-down perspective, highlighting its symmetrical design. The pins are arranged in two rows of 14 pins each. The top row of pins is numbered 1 through 16, and the bottom row is numbered 17 through 28. The package features a central notch and two mounting tabs on the sides. The diagram is a black and white line drawing, typical of technical documentation.

A detailed diagram of a 30-pin connector, likely a D-subminiature connector. The connector has a central body with two rows of pins. The top row contains pins 1 through 30, and the bottom row contains pins 31 through 60. The pins are numbered 1 through 30 in the diagram. The connector has a locking mechanism on the right side.

[illegible]

A diagram of a two-story building. The ground floor has two rooms labeled '1' and '2'. The first floor is empty.

1	2	3	4	5	6	7	8	9	10
11	12	13	14	15	16	17	18	19	20
21	22	23	24	25	26	27	28	29	30

A diagram of a vertical document layout. It features a header at the top, followed by two numbered sections labeled '1' and '2' stacked vertically. At the bottom, there is a footer area containing a small logo on the left and a small rectangular box on the right.

Diagram illustrating a vertical document structure. The document contains a header, a table with two rows labeled 1 and 2, and a footer.

Diagram illustrating a vertical document structure with a header, two numbered boxes (1 and 2), and a footer.

A diagram of a vertical document layout. It features a header at the top, followed by two numbered sections labeled '1' and '2' stacked vertically, and a footer at the bottom.

Diagram illustrating a vertical document structure with a header box, a table with two rows (1 and 2), and a footer box.

A diagram of a vertical document layout. It features a header at the top, followed by two numbered sections labeled '1' and '2' stacked vertically. At the bottom, there is a footer area.

A diagram of a 22-pin D-sub connector. The pins are arranged in two rows of 11 pins each. The top row is numbered 1 through 11 from left to right. The bottom row is numbered 12 through 22 from left to right. The connector has a D-shaped shield and a latch mechanism on top.

A diagram of a 22-pin D-sub connector. The connector is shown from a top-down perspective, with the pins arranged in two rows of 11. The pins are numbered 1 through 22, starting from the top left and moving right, then down to the bottom left and moving right. The connector has a D-shaped shield and a latch mechanism on the right side.

1	2	3	4	5	6	7	8	9	10	11
12	13	14	15	16	17	18	19	20	21	22

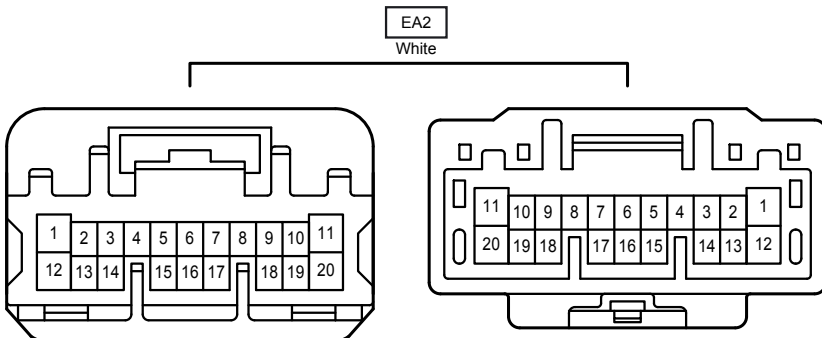
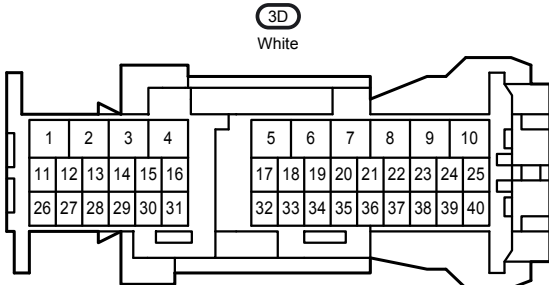
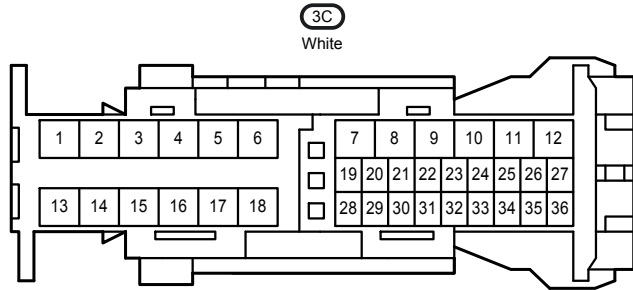
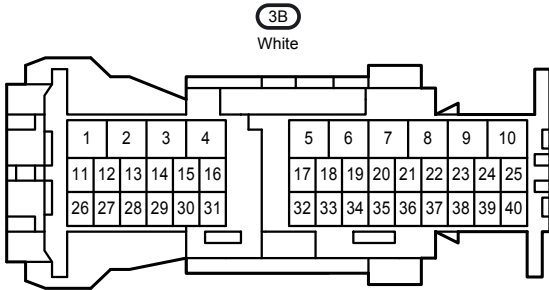
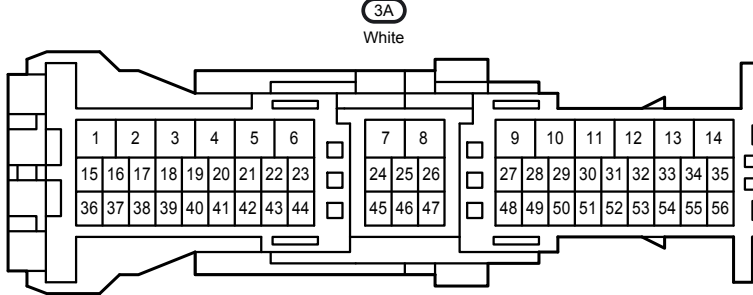
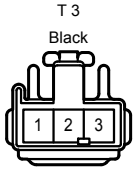
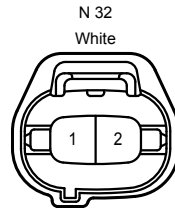
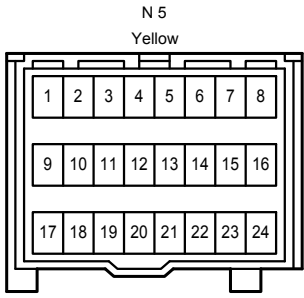
A diagram of a 4-bit shift register. It consists of four rectangular cells arranged horizontally, labeled 1, 2, 3, and 4 from left to right. Above the cells are two input lines, each with a small rectangular block above it. Below the cells are two output lines, each with a small rectangular block below it.

1	2	3	4
5	6	7	8
9	10		

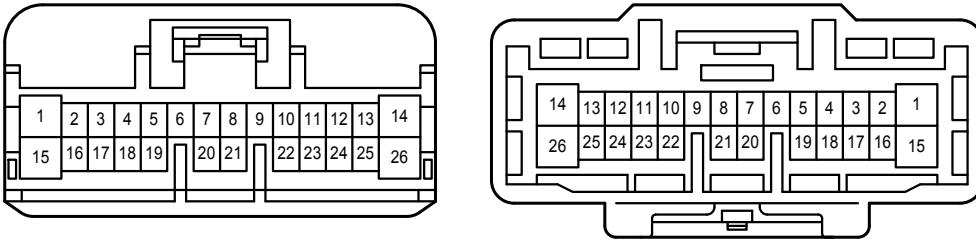
A diagram of a 18-pin DIP package. The package is rectangular with rounded corners and a double-line border. Inside, there are two rows of pins. The top row has pins numbered 1 through 9 from left to right. The bottom row has pins numbered 10 through 18 from left to right. Above the top row of pins, there are several horizontal lines of varying lengths, representing the internal circuitry or a specific pin configuration. The pins are represented by small rectangles with a central dot.

1	2	3	4
5	6	7	8
9	10		

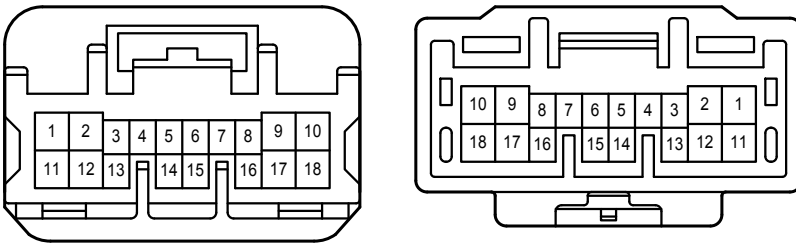
Door Lock Control



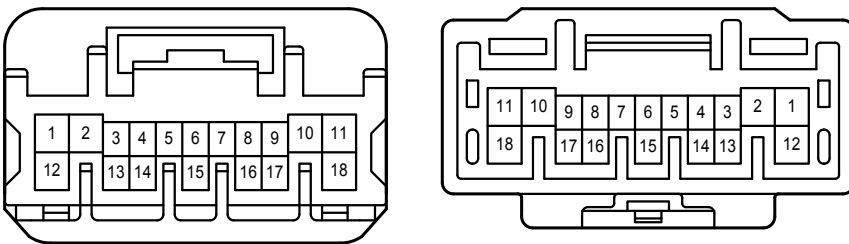
IA3
Blue



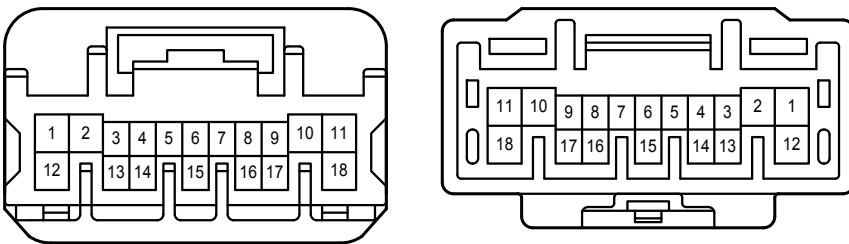
IA8
Gray



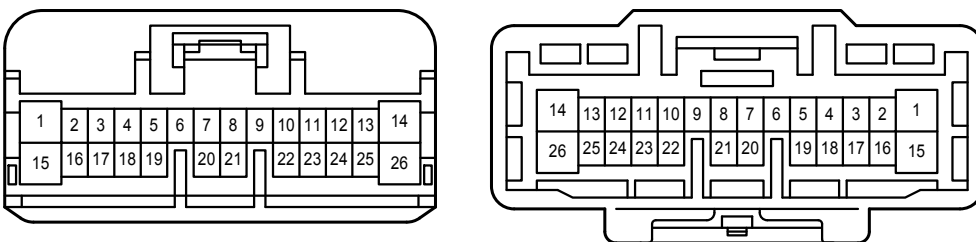
IN1
White



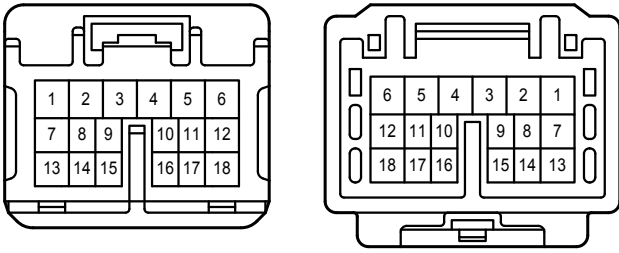
IO1
White



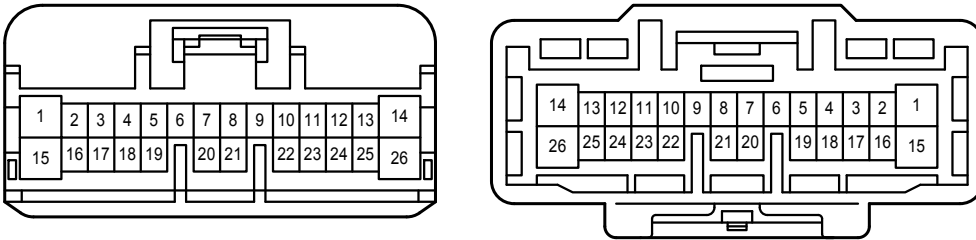
IO2
White



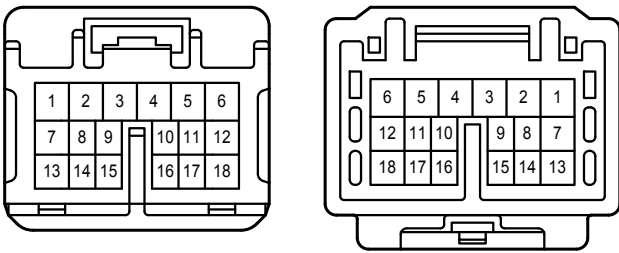
JI1
Gray



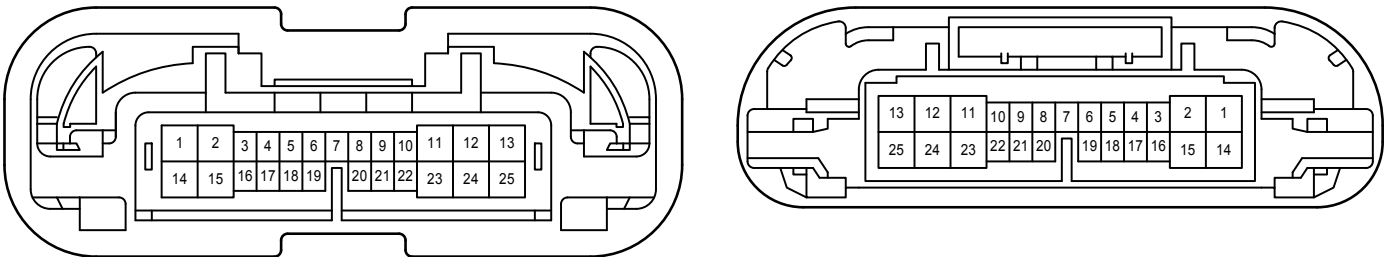
JI2
Blue



KI1
White

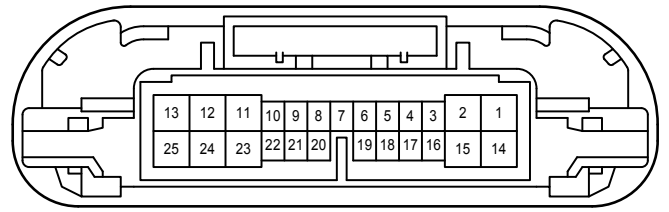
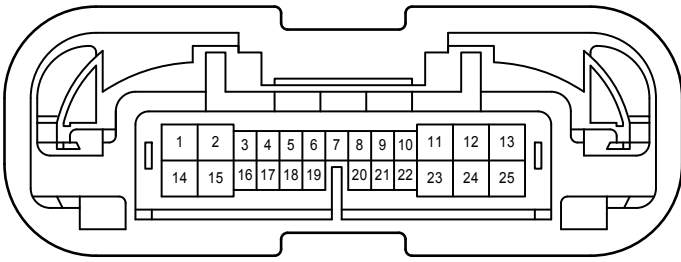


LO1
White



Door Lock Control

MN1
White



TN1
White

