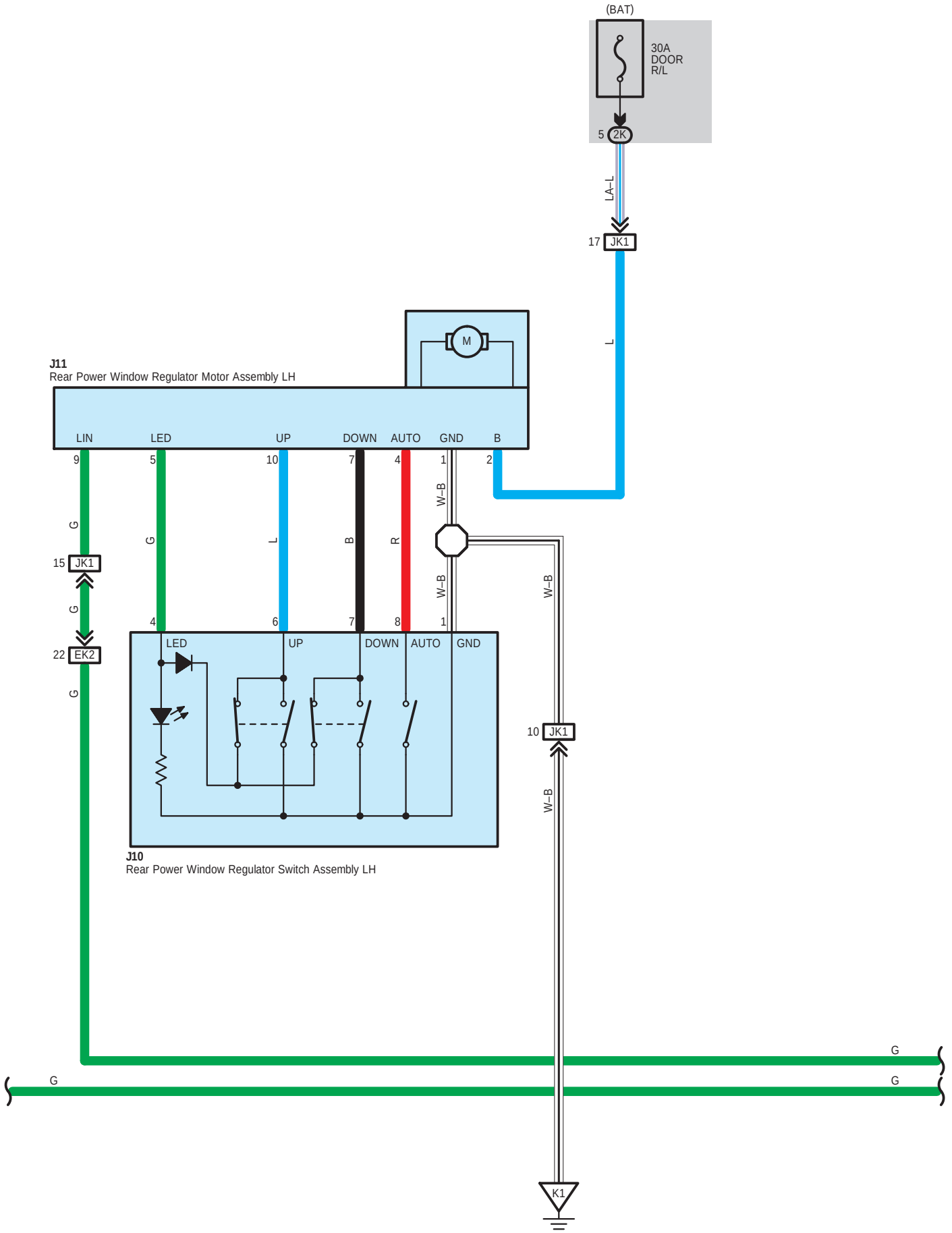
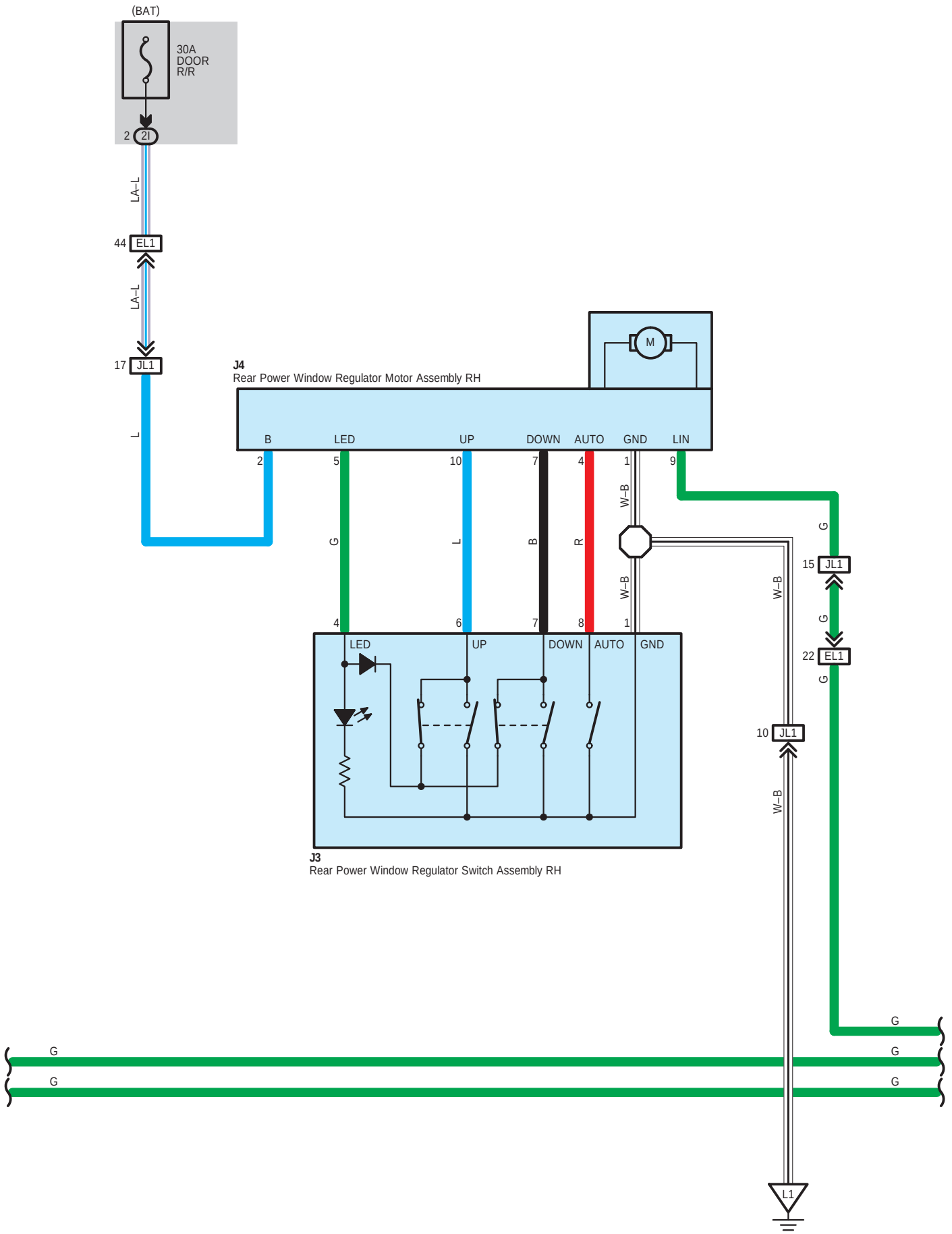
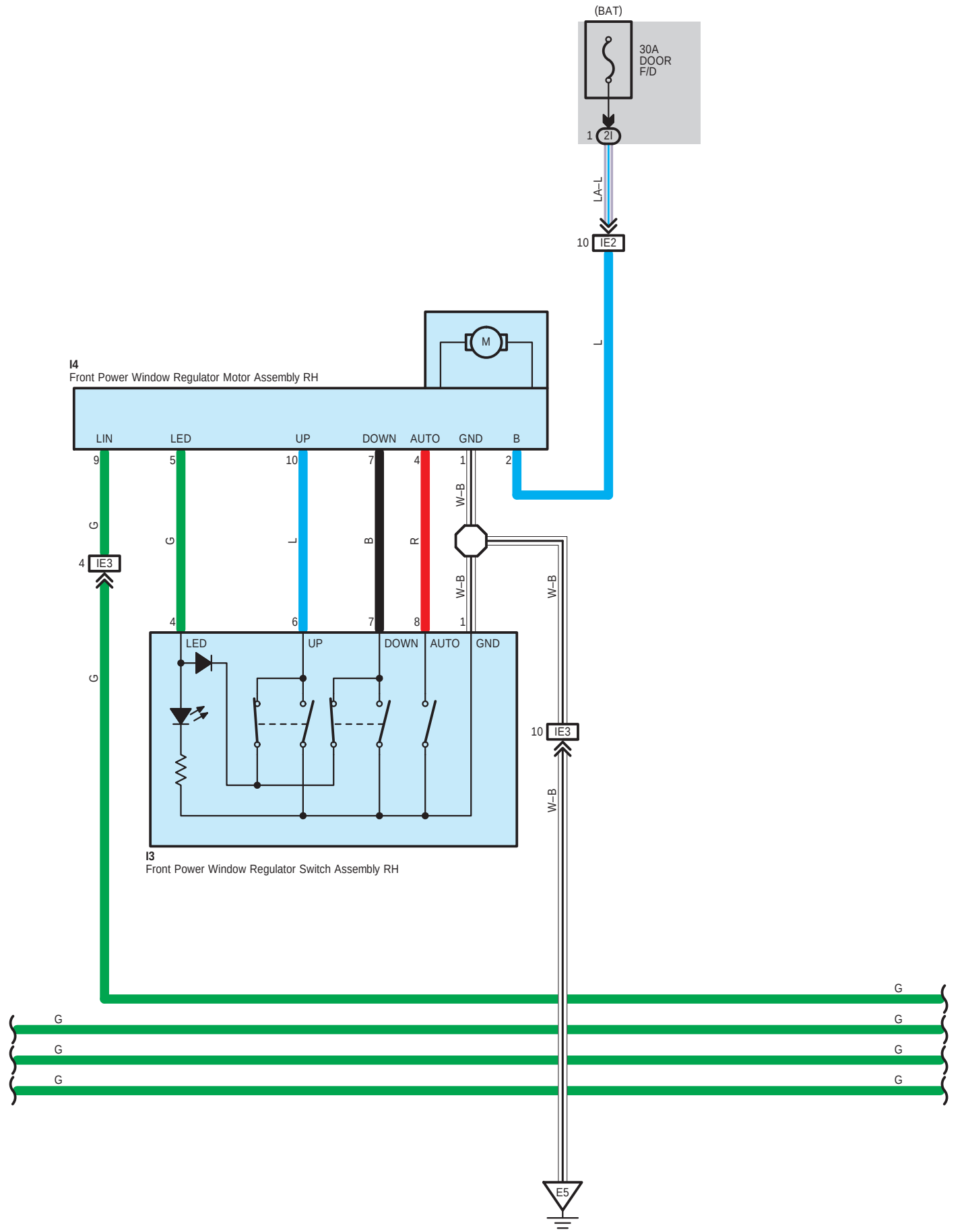
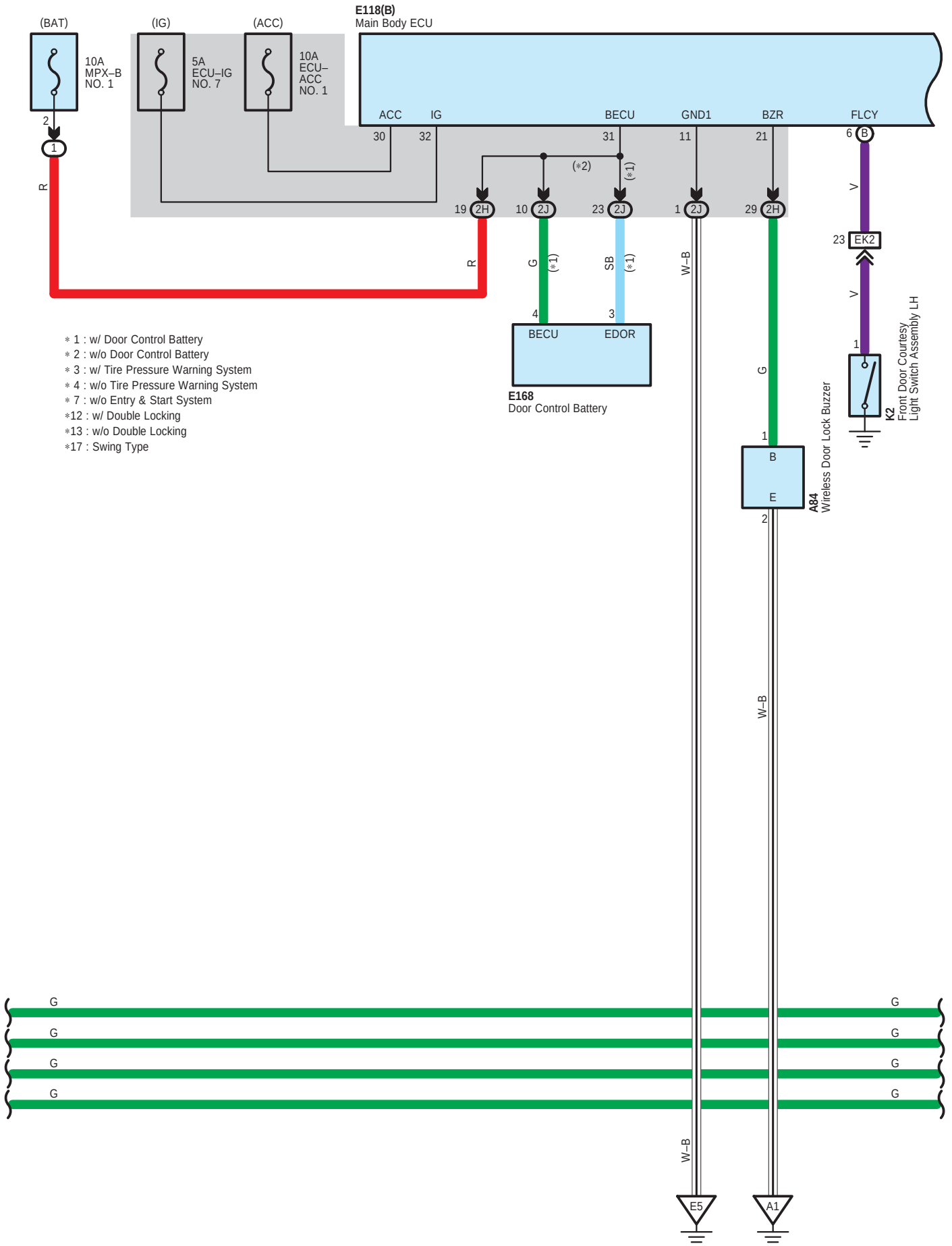


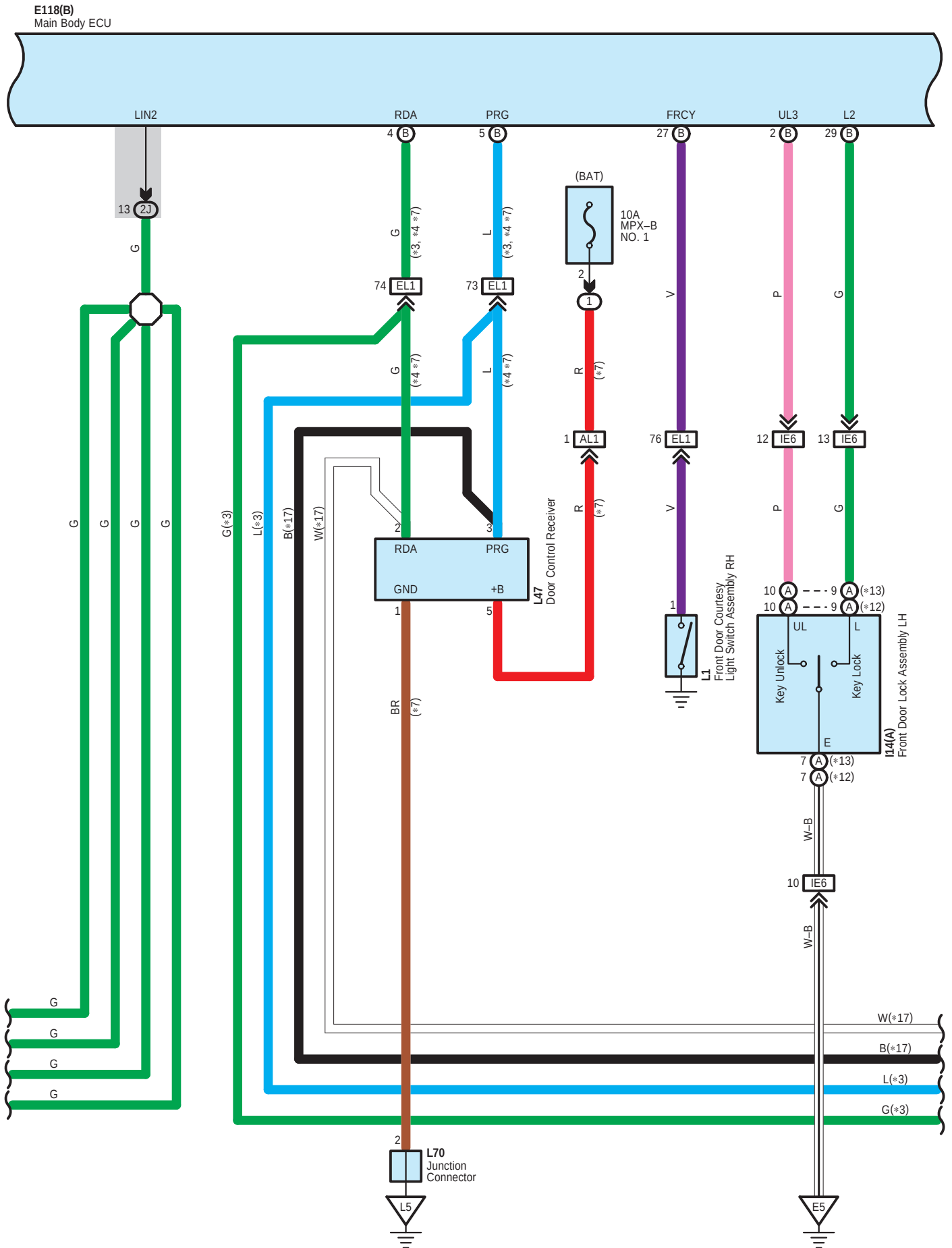


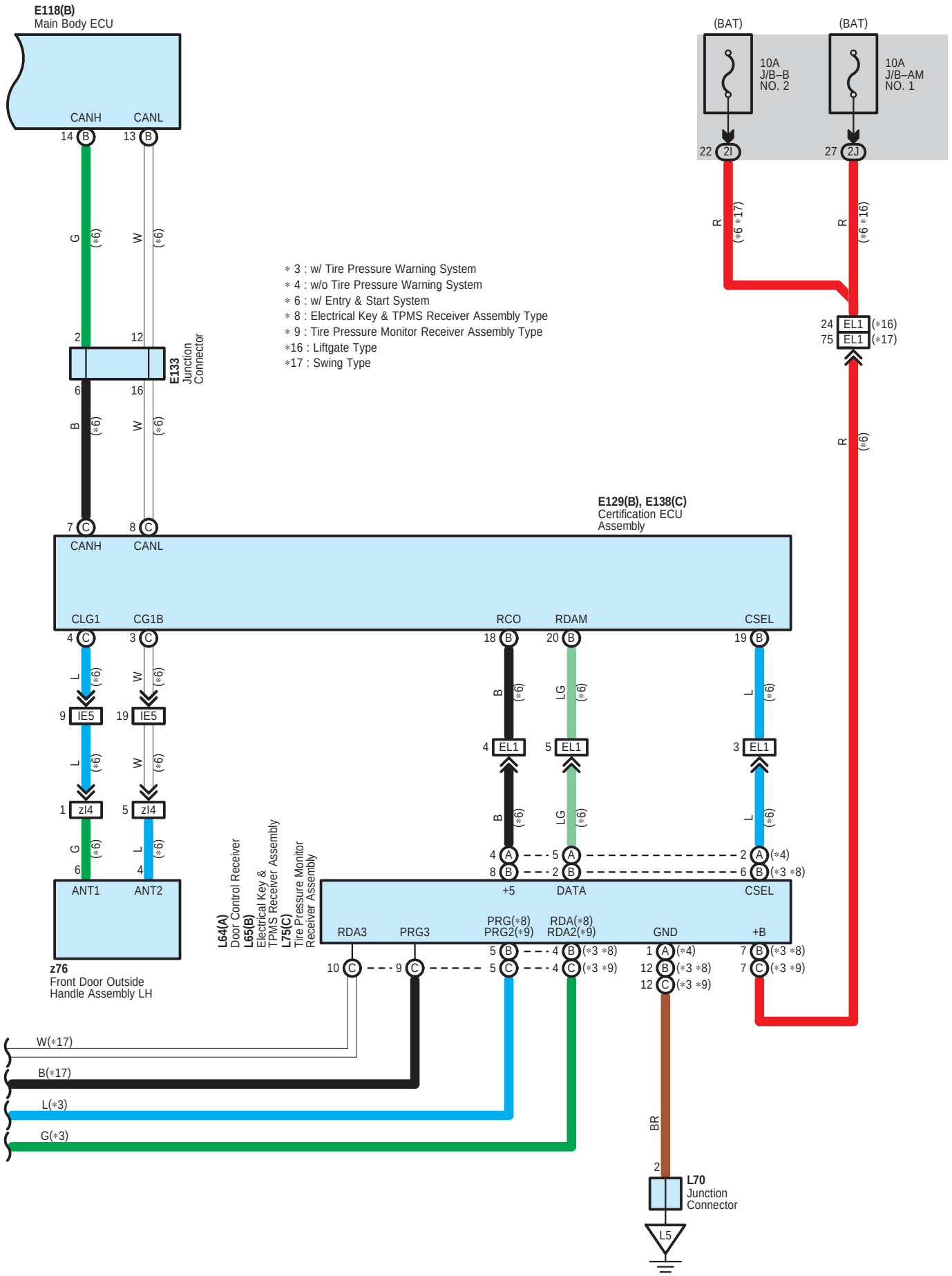












A detailed diagram of a 30-pin connector, likely a D-subminiature connector. The pins are arranged in three rows of ten. The top row contains pins 1, 2, 3, followed by a large central cutout, then pins 4, 5, 6. The middle row contains pins 7, 8, 9, followed by a small central cutout, then pins 10, 11, 12, followed by another small central cutout, then pins 13, 14, 15, followed by another small central cutout, then pins 16, 17, 18. The bottom row contains pins 19, 20, 21, followed by a small central cutout, then pins 22, 23, 24, followed by another small central cutout, then pins 25, 26, 27, followed by another small central cutout, then pins 28, 29, 30. The connector has a central locking mechanism and two side latches.

The diagram illustrates a 16-bit parallel adder. It consists of two 8-bit registers, each with an input and an output. The outputs of these registers are connected to a 16-bit ALU (Arithmetic Logic Unit). The ALU has two 8-bit inputs and a 16-bit output. The ALU is represented by a large rectangle with a smaller rectangle inside it, and a label '16-bit ALU' below it.



A diagram of a 12-pin D-sub connector. It features a 2x6 grid of pins. The top row is labeled 1 through 6, and the bottom row is labeled 7 through 12. The connector has a D-shaped shield and a latch mechanism.

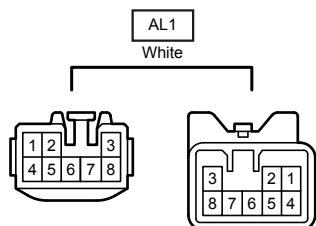
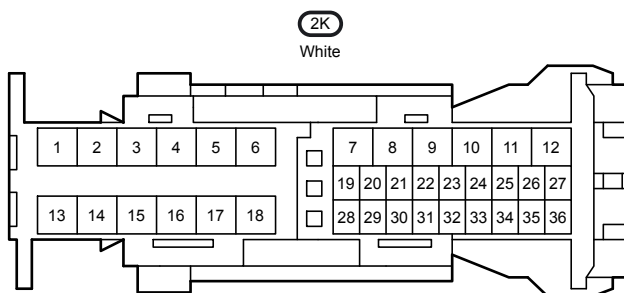
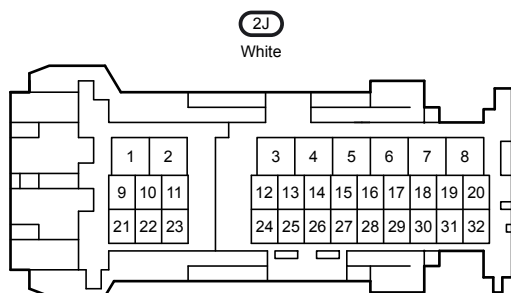
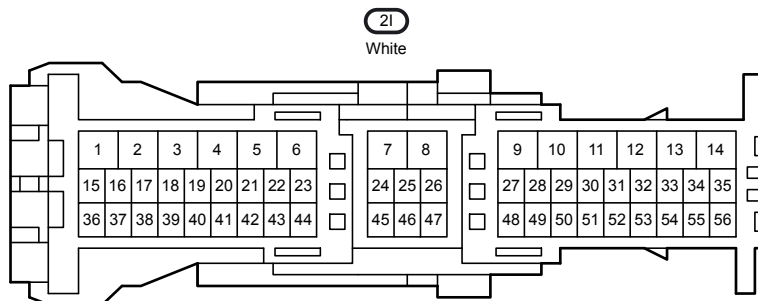
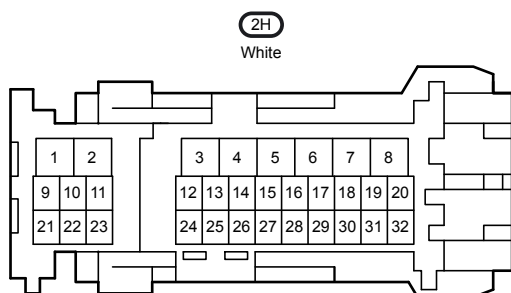
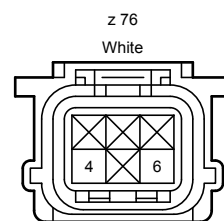
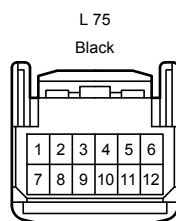
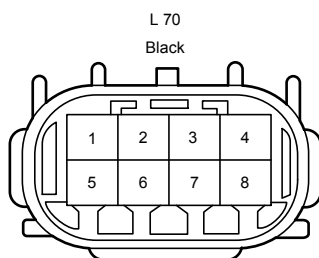
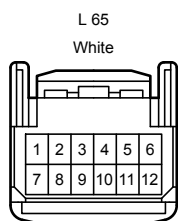
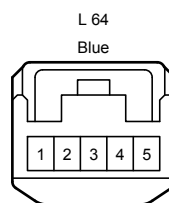
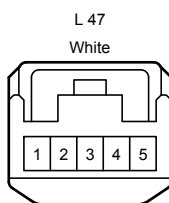
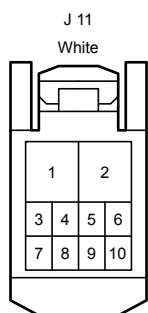
A diagram of a 4x4 grid with numbers 1 through 8. The grid is divided into four quadrants by a horizontal and vertical line. The numbers are arranged as follows:

1	2	3	4
5	6	7	8

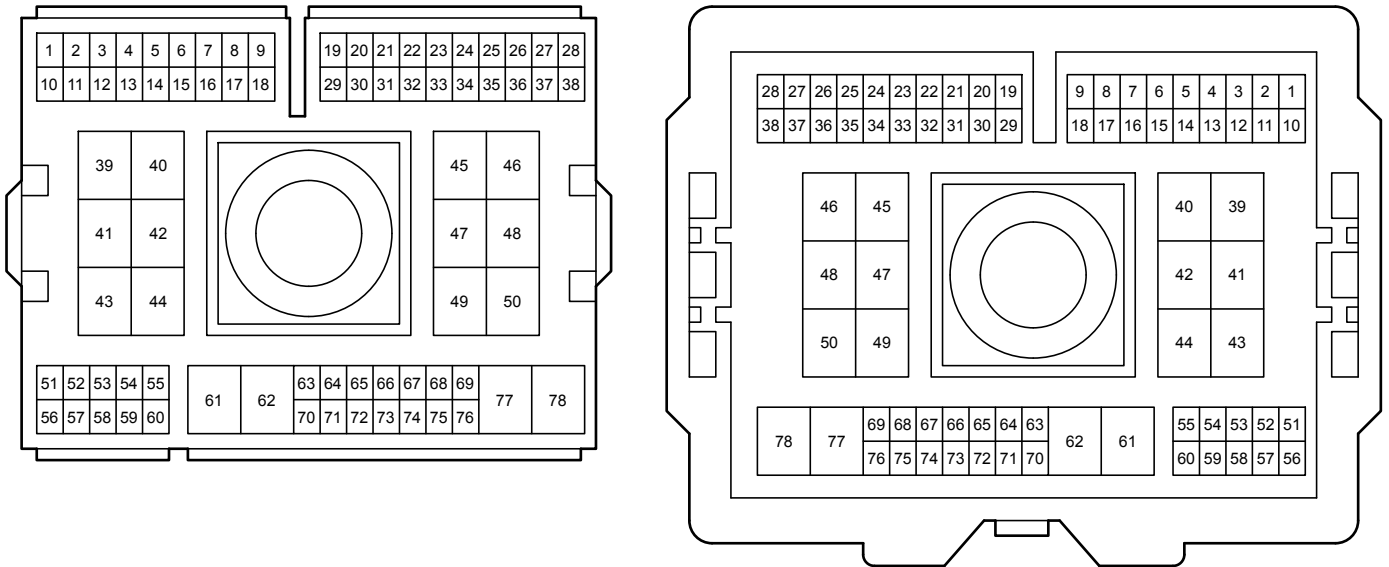
1	2	3	4	5	6	7	8	9	10
11	12	13	14	15	16	17	18	19	20

A diagram of a 4x4 grid with numbers 1 through 8. The grid is divided into four quadrants by a vertical line and a horizontal line. The numbers are arranged as follows:

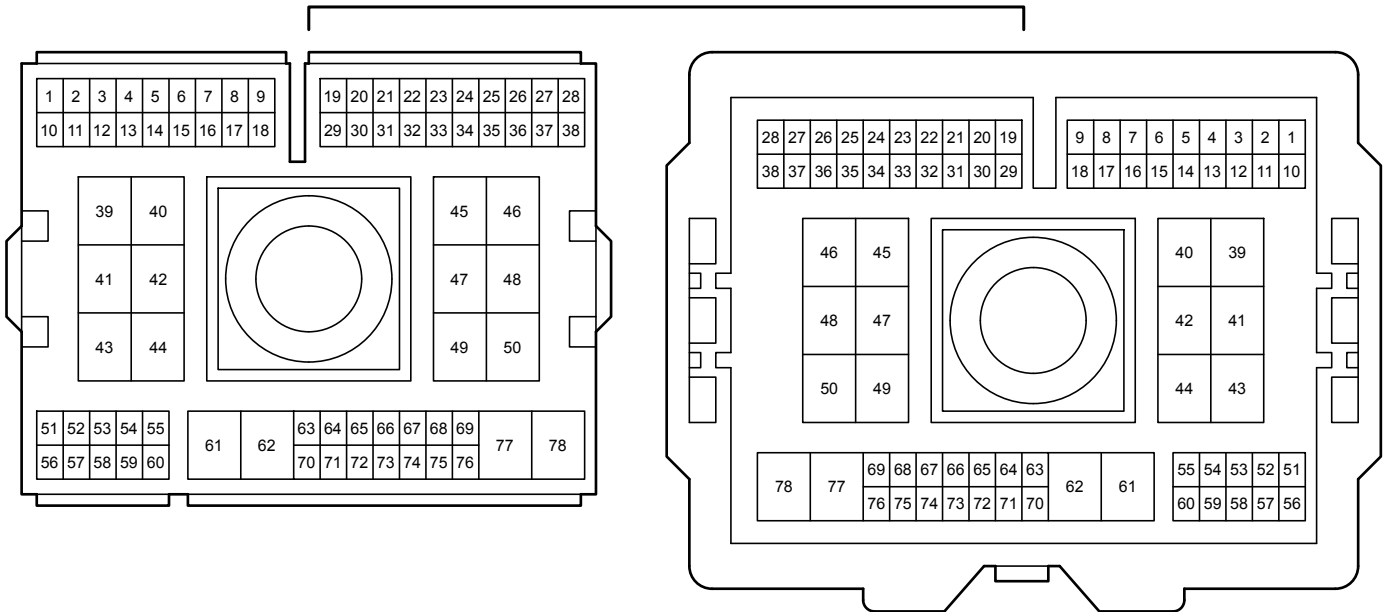
1	2	3	4
5	6	7	8



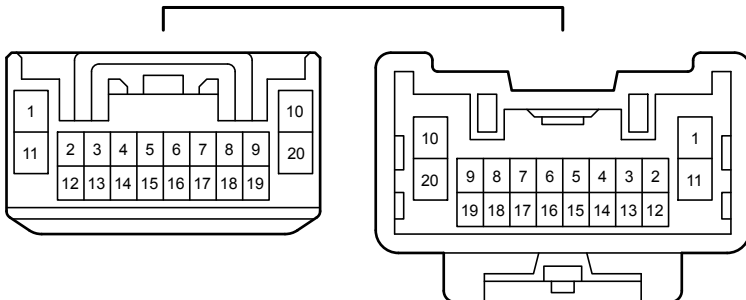
EK2
White



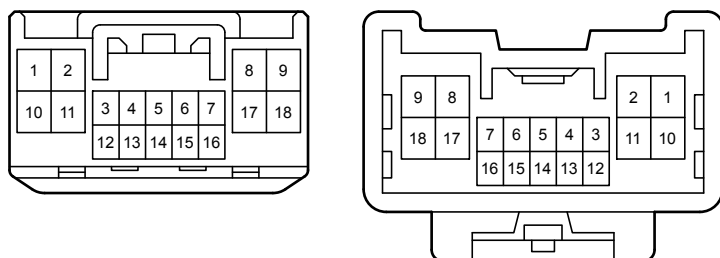
EL1
White



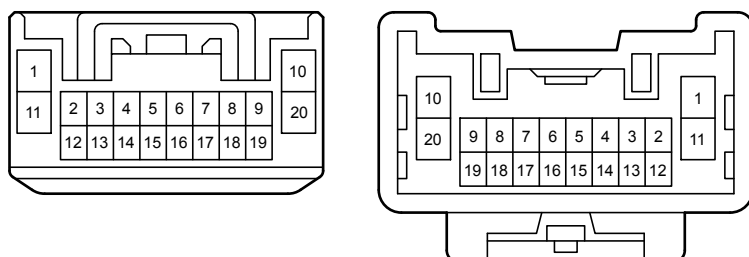
IE2
Blue



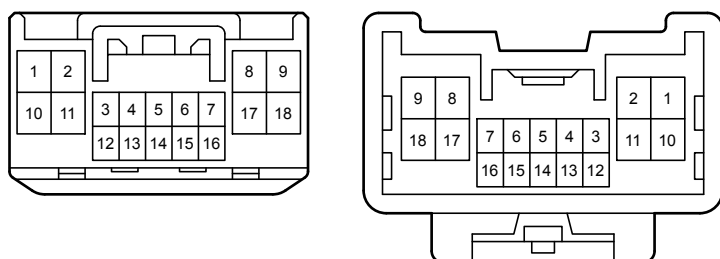
IE3
White



IE5
Blue



IE6
White



JK1
White

