

Fig 1: Computer Data Lines Circuit (1 of 3)

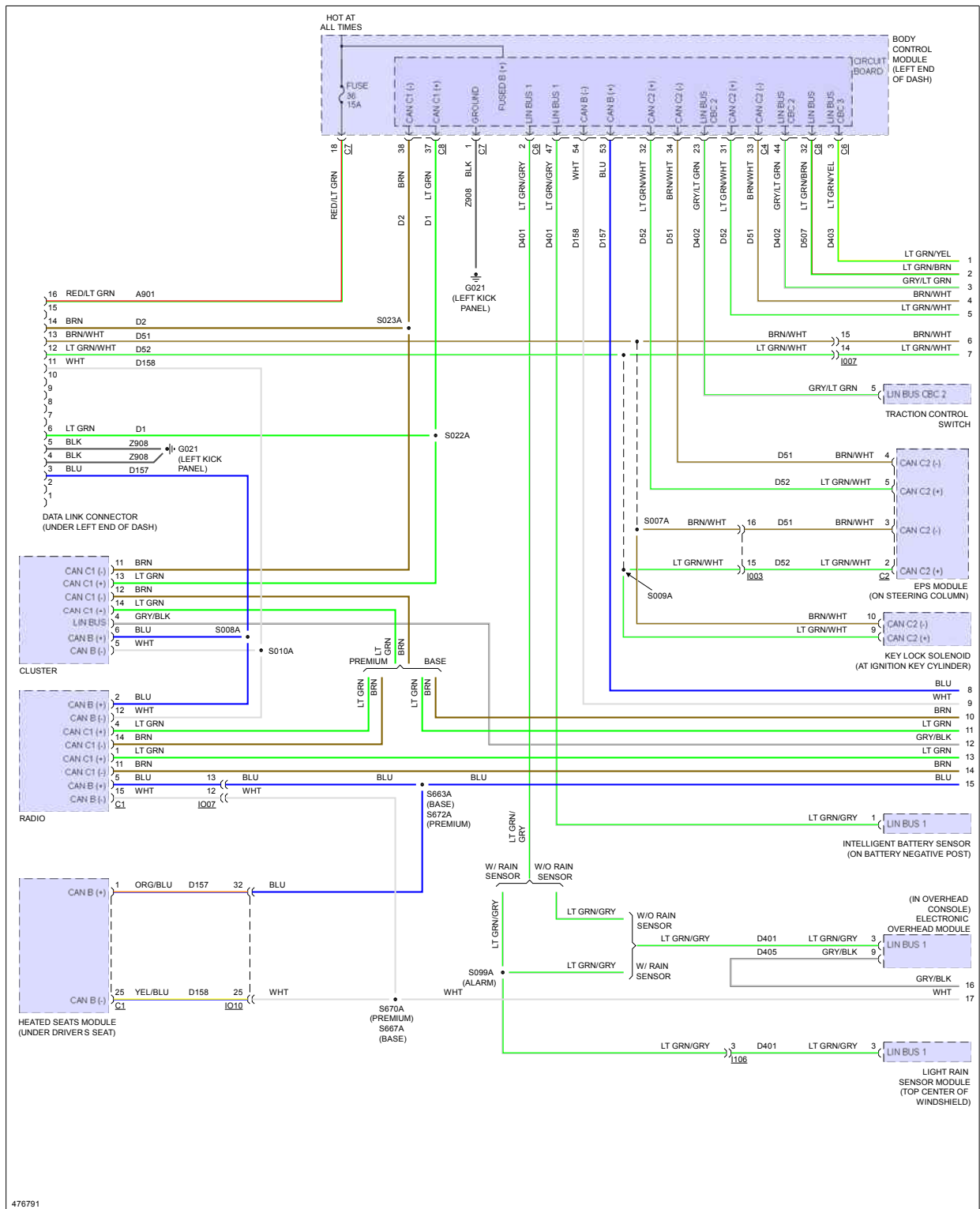


Fig 2: Computer Data Lines Circuit (2 of 3)



Fig 3: Computer Data Lines Circuit (3 of 3)

